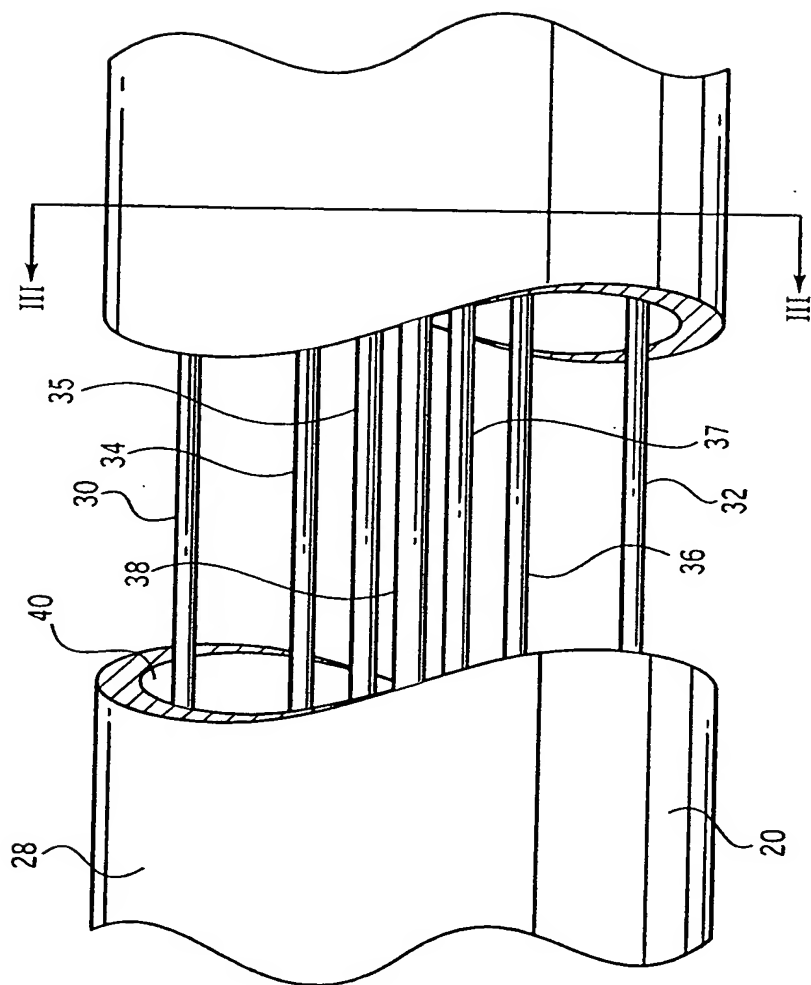


Fig. 1



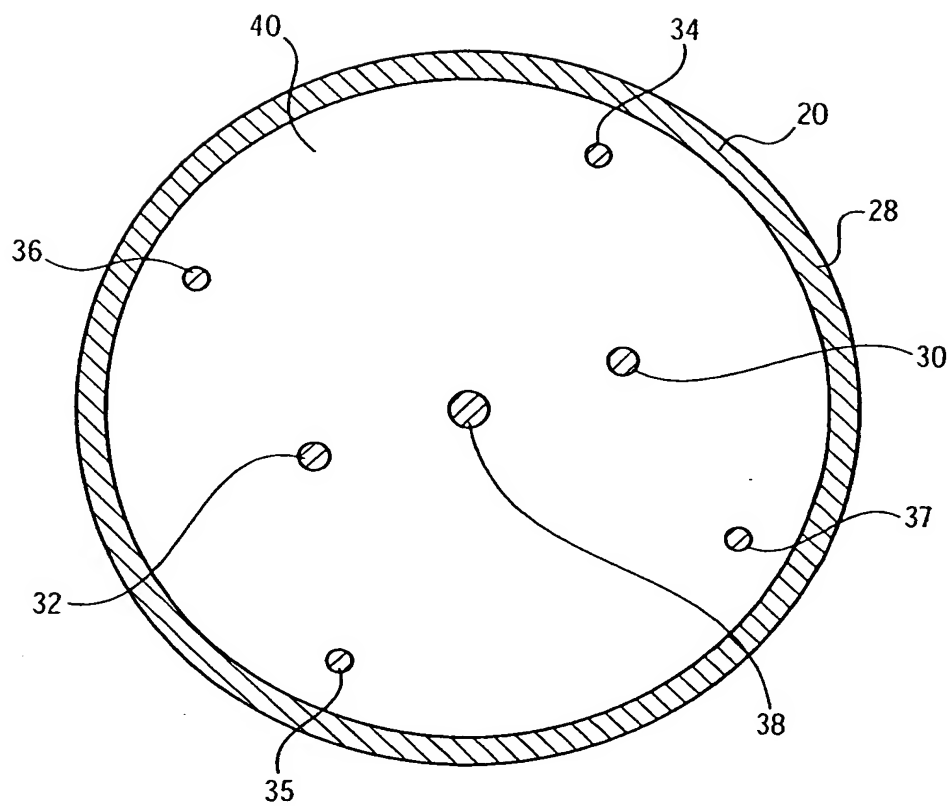
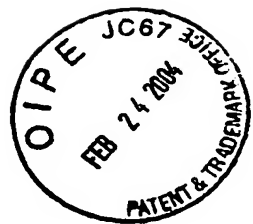


Fig. 3

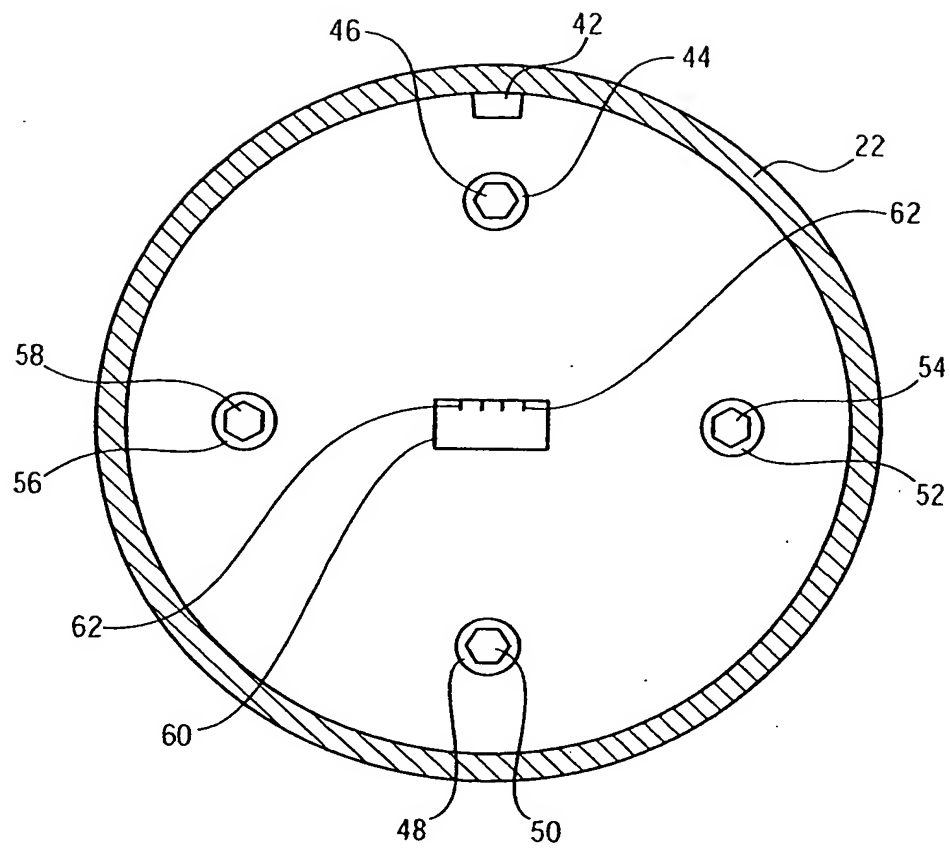


Fig. 4

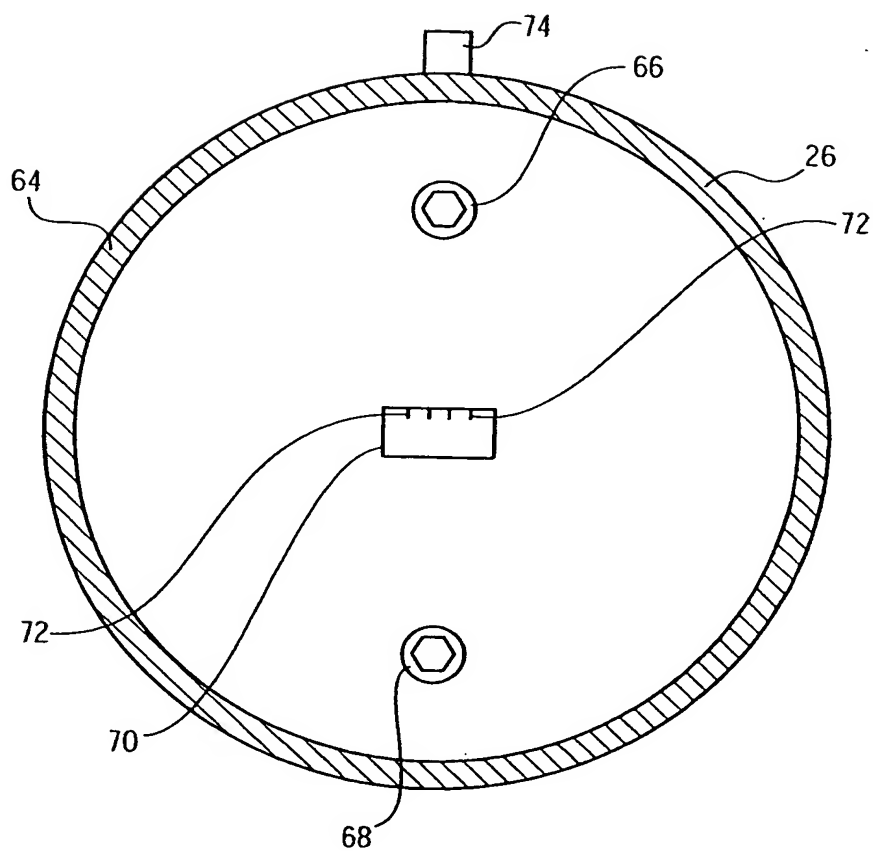
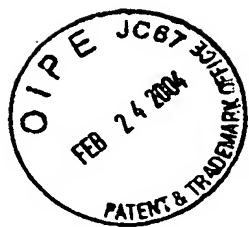


Fig. 5

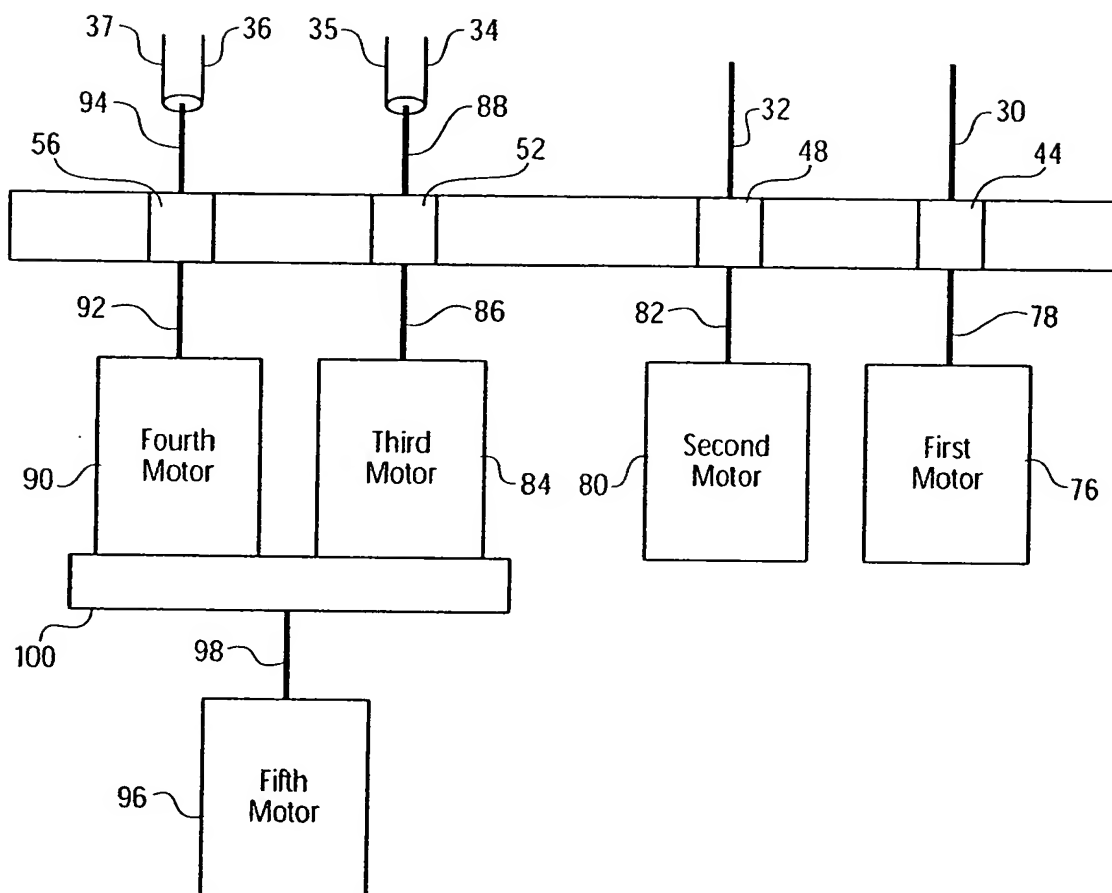
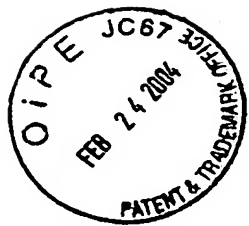


Fig.6

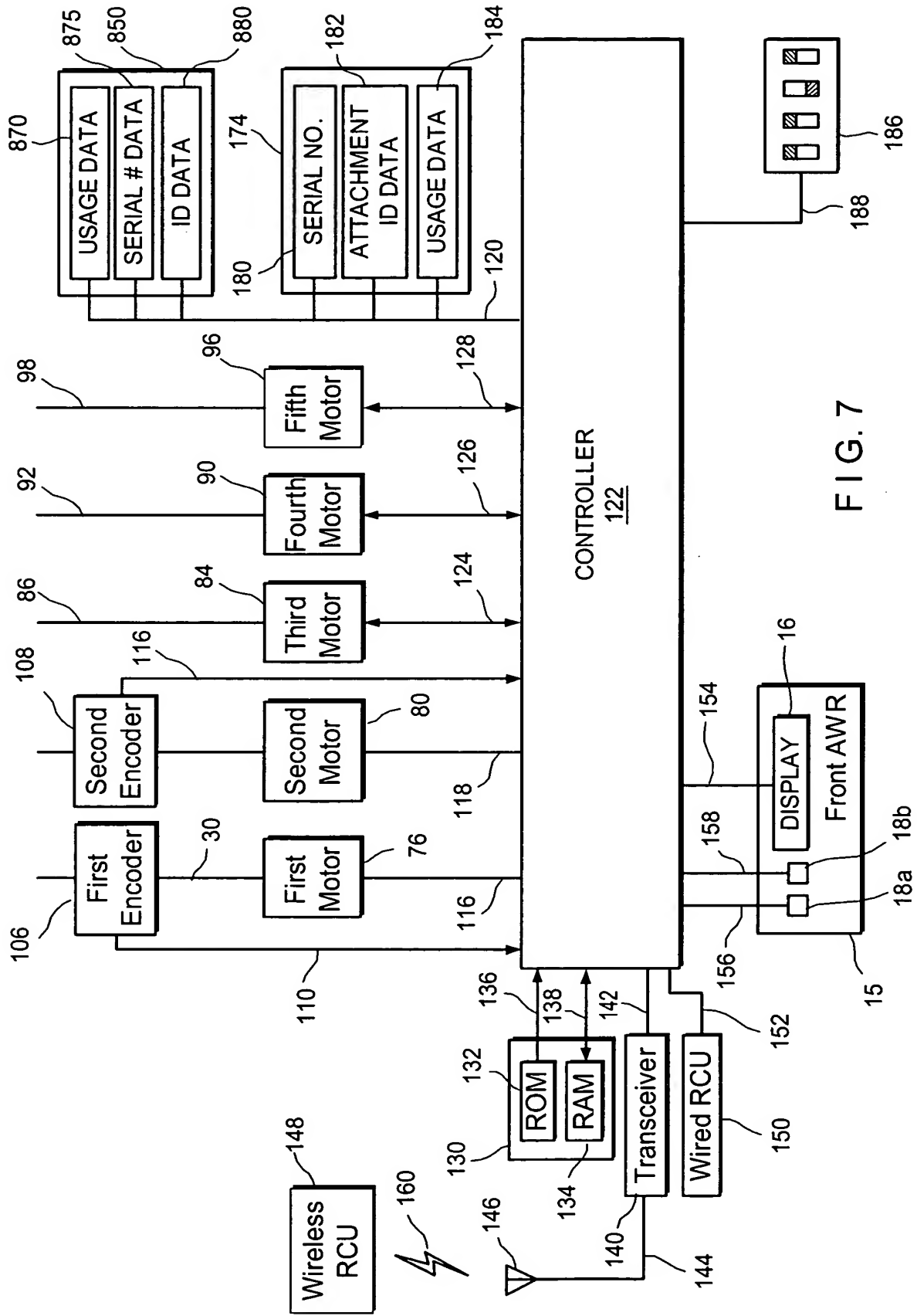


FIG. 7

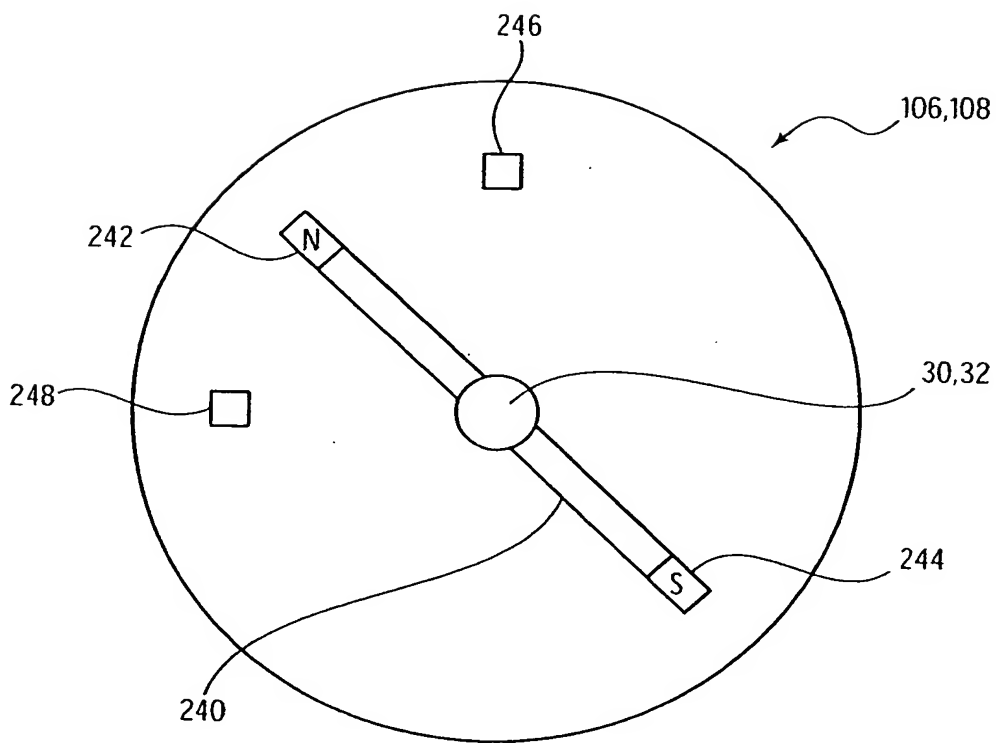
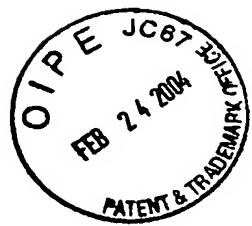


Fig. 8

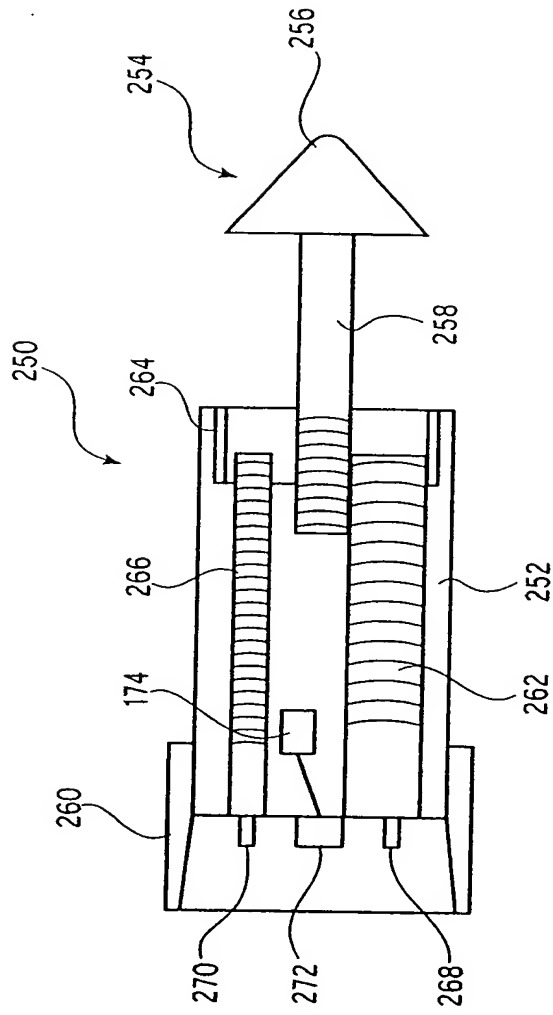


Fig. 9 A

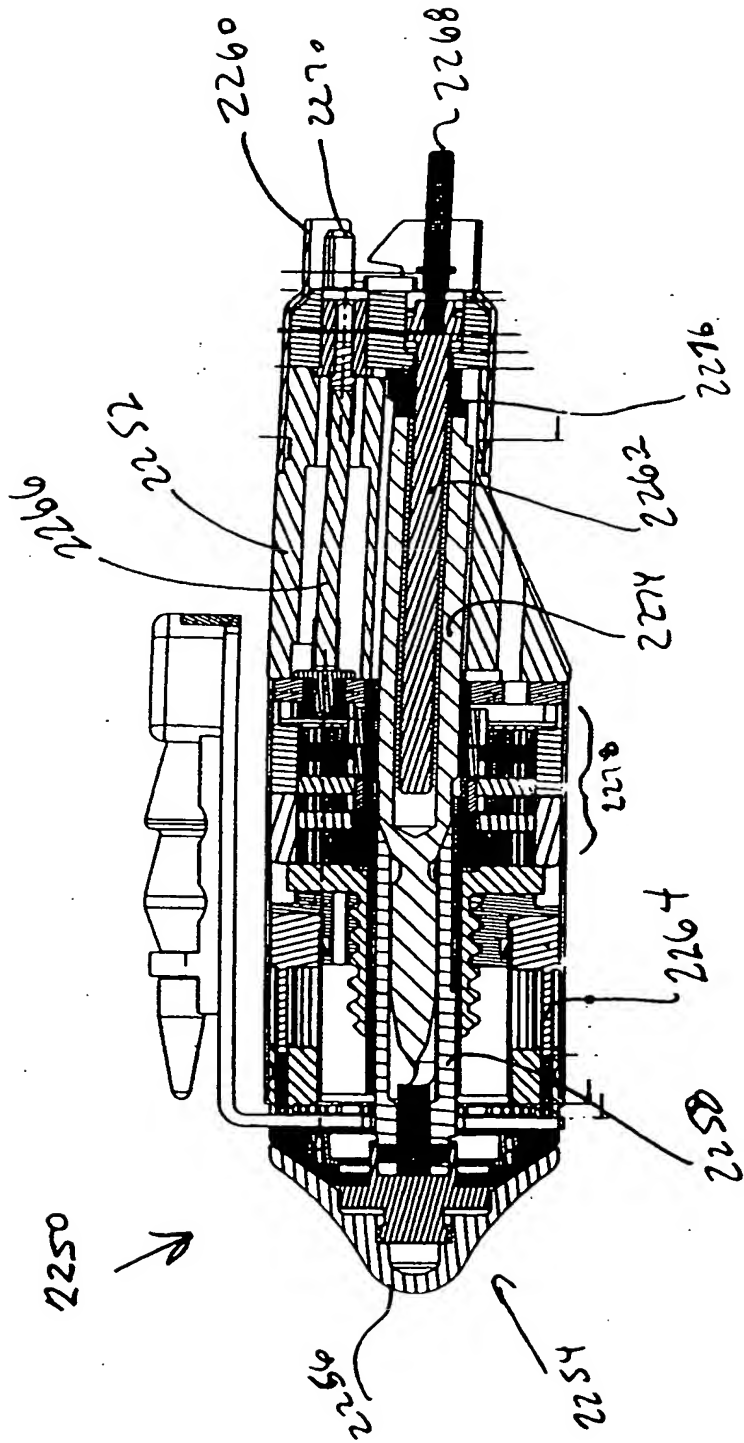


Figure 98

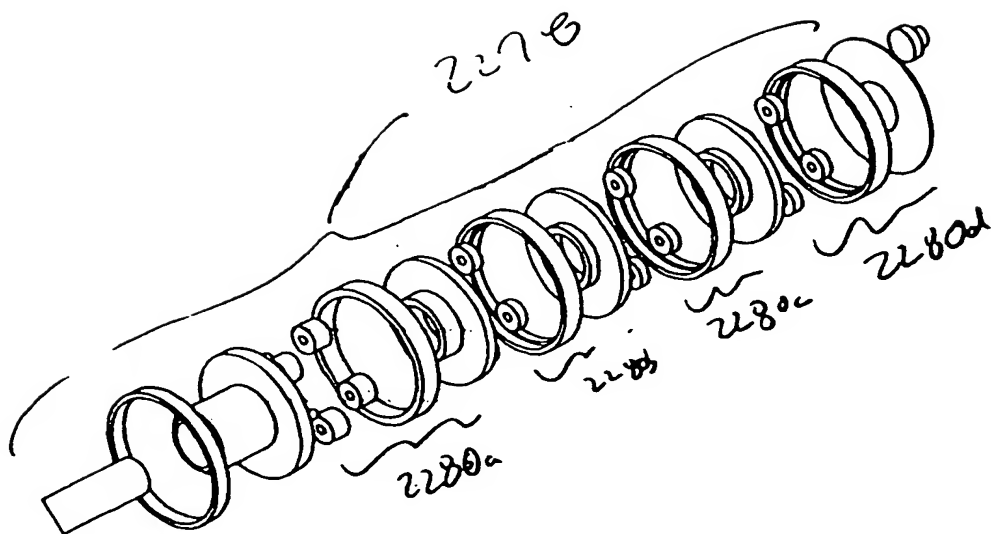


FIG. 9C

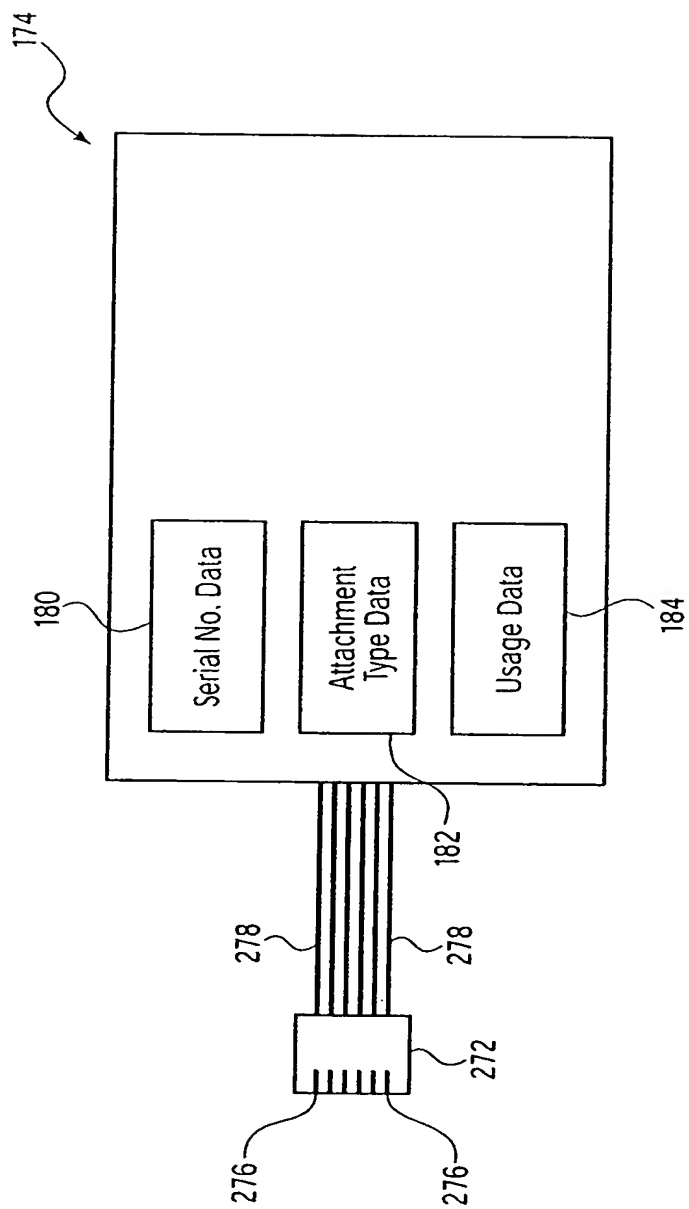
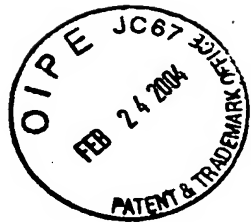


Fig. 10

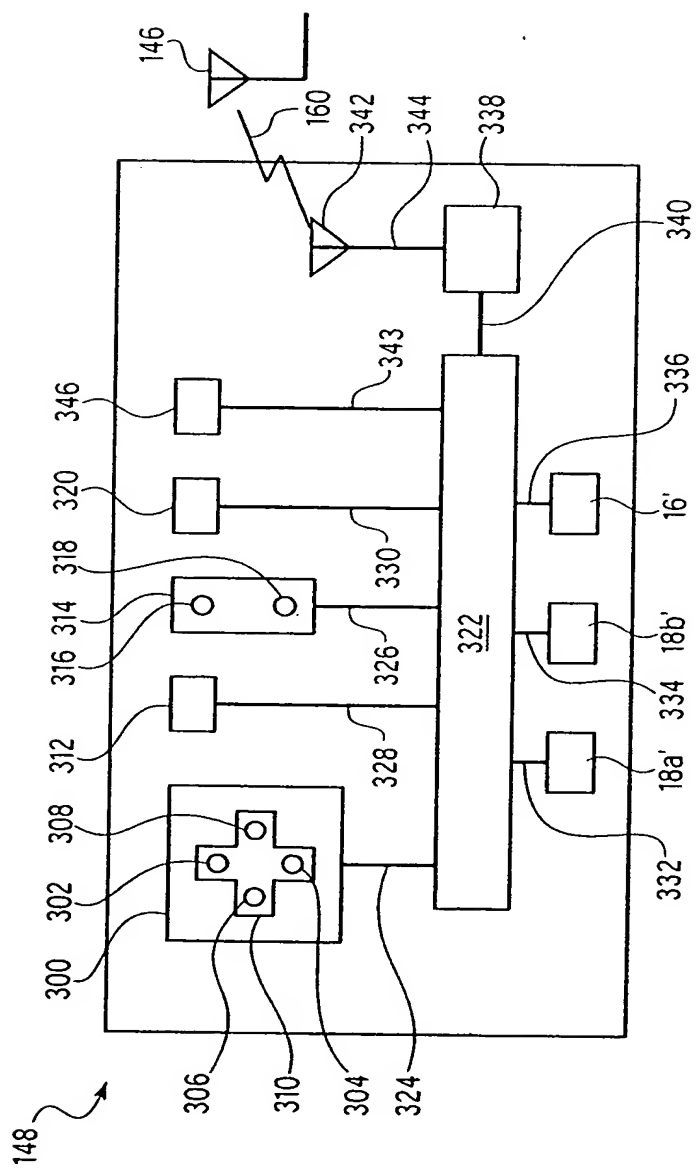
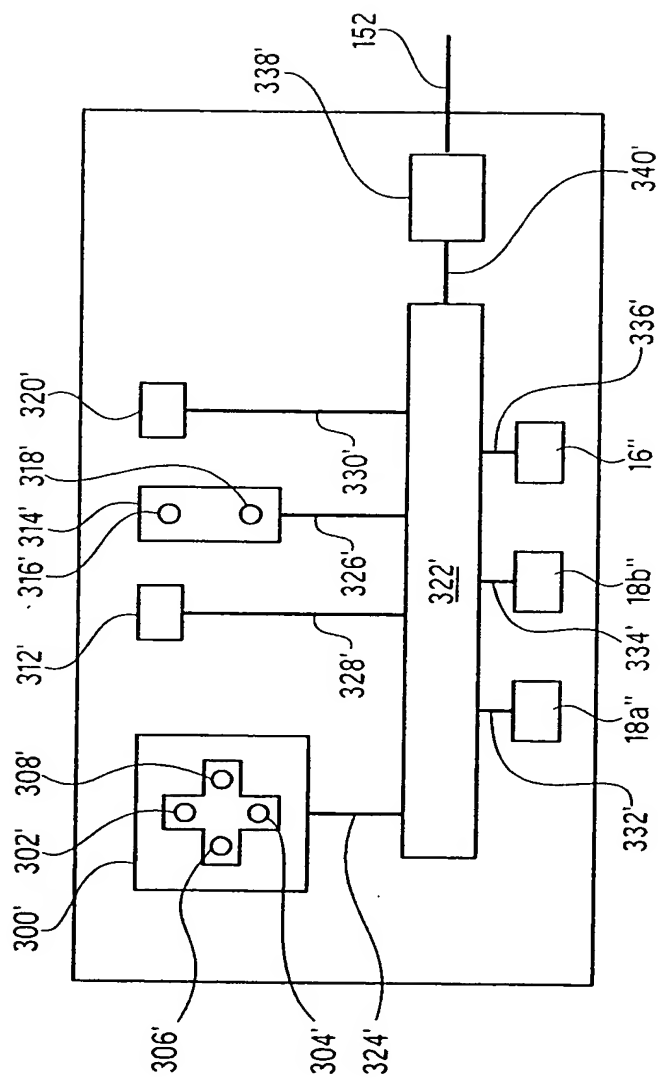


Fig. 11



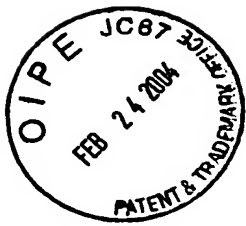


Fig. 13

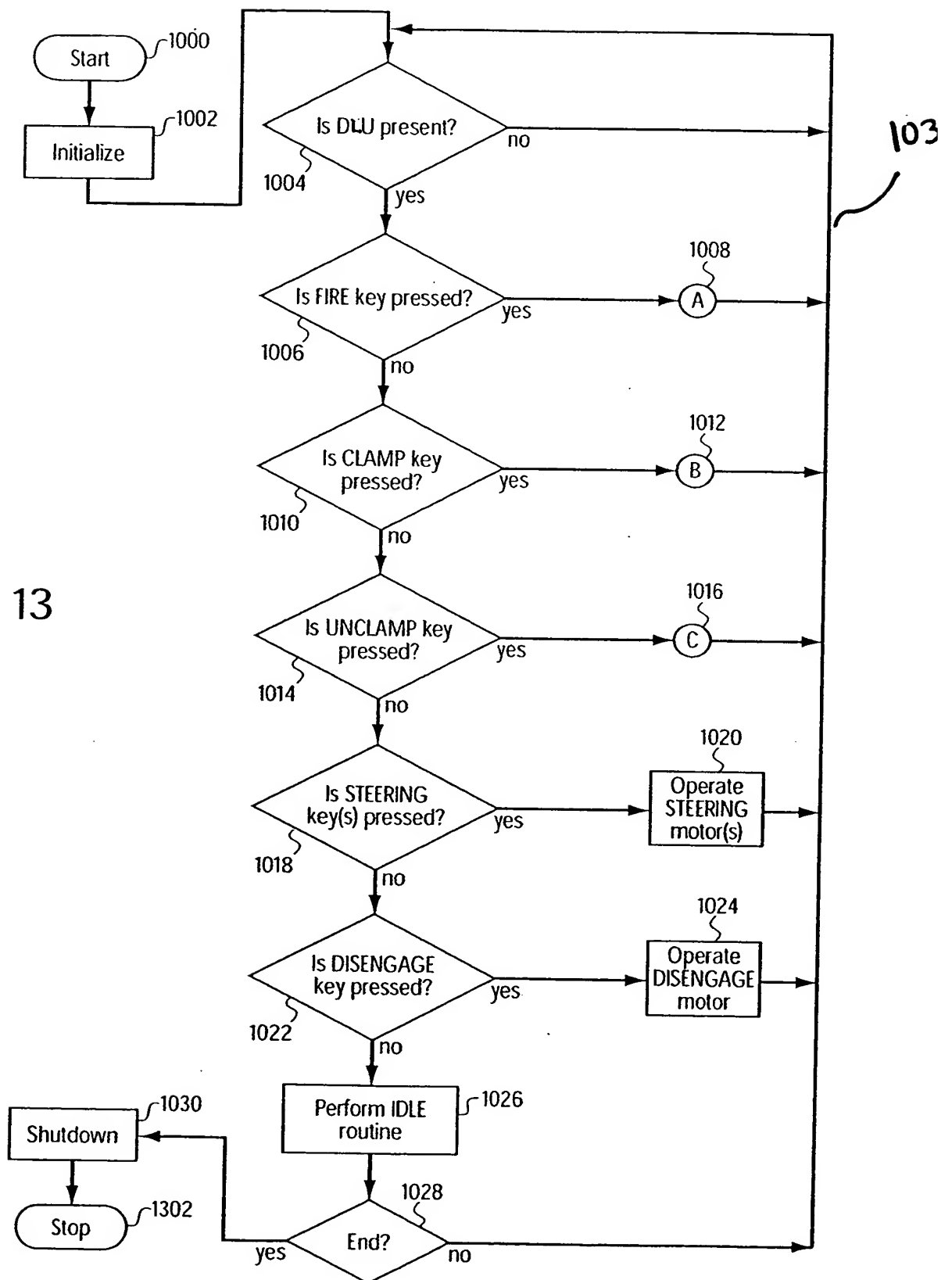
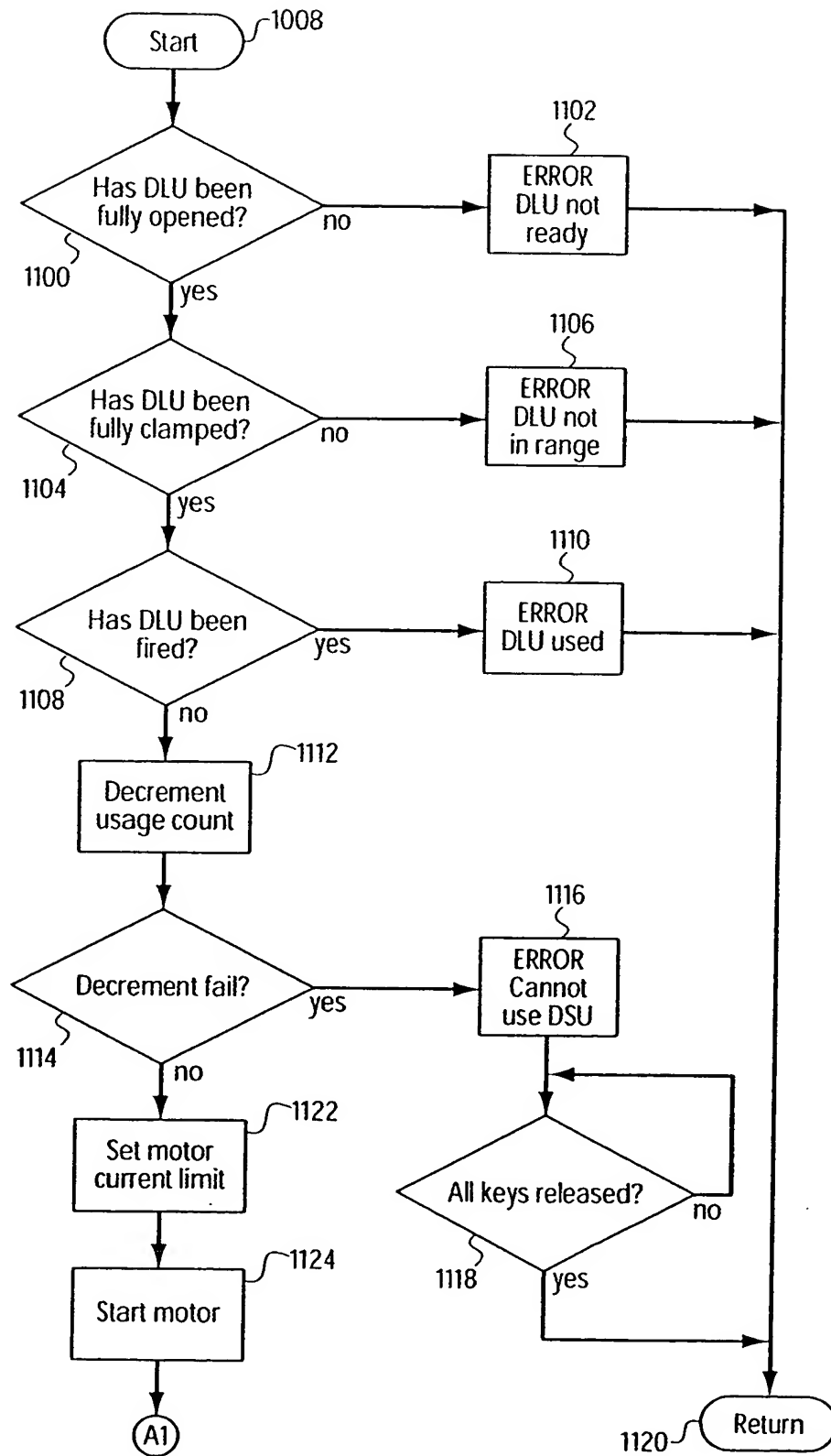




Fig. 14a



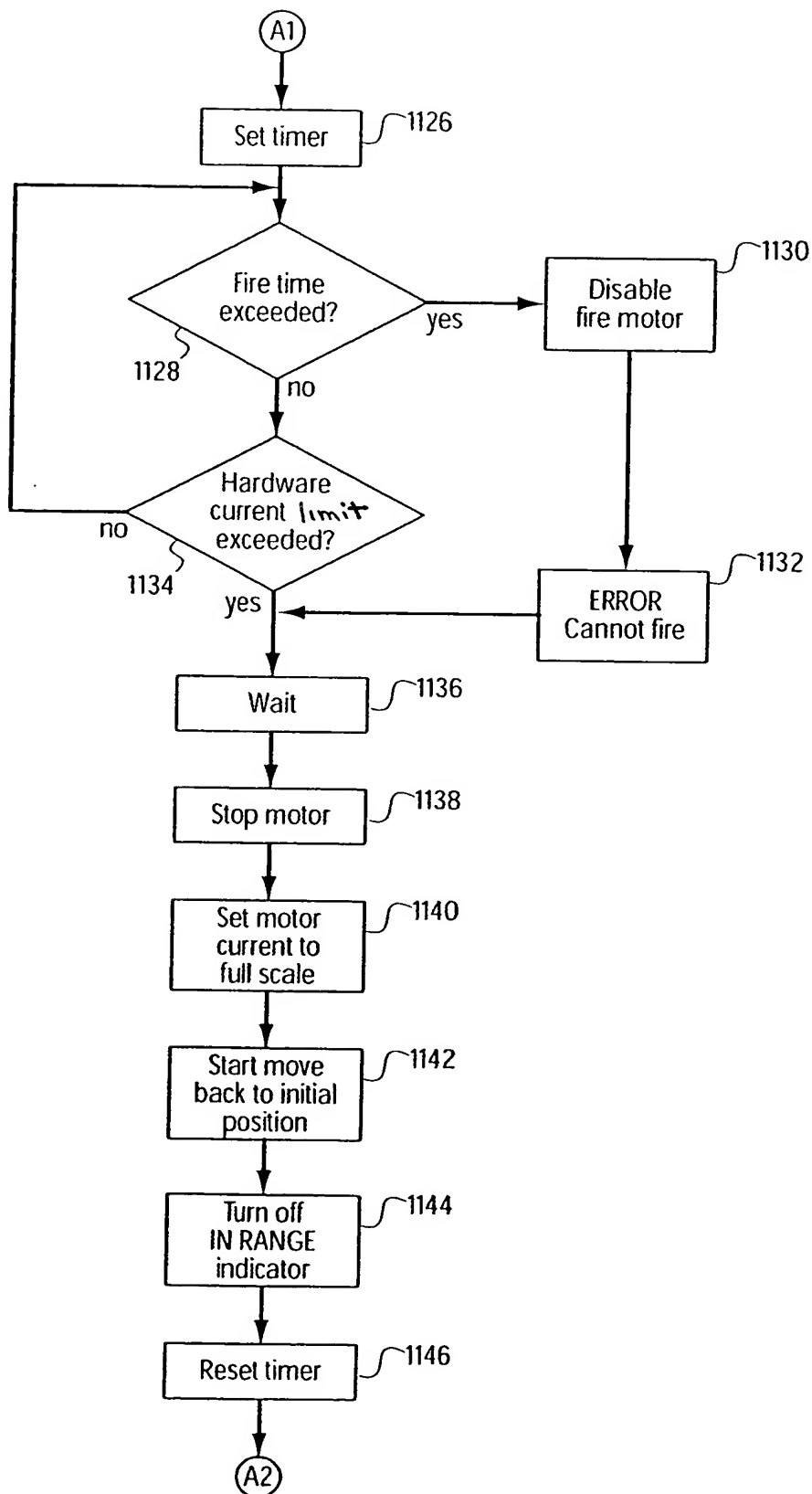


Fig. 14b

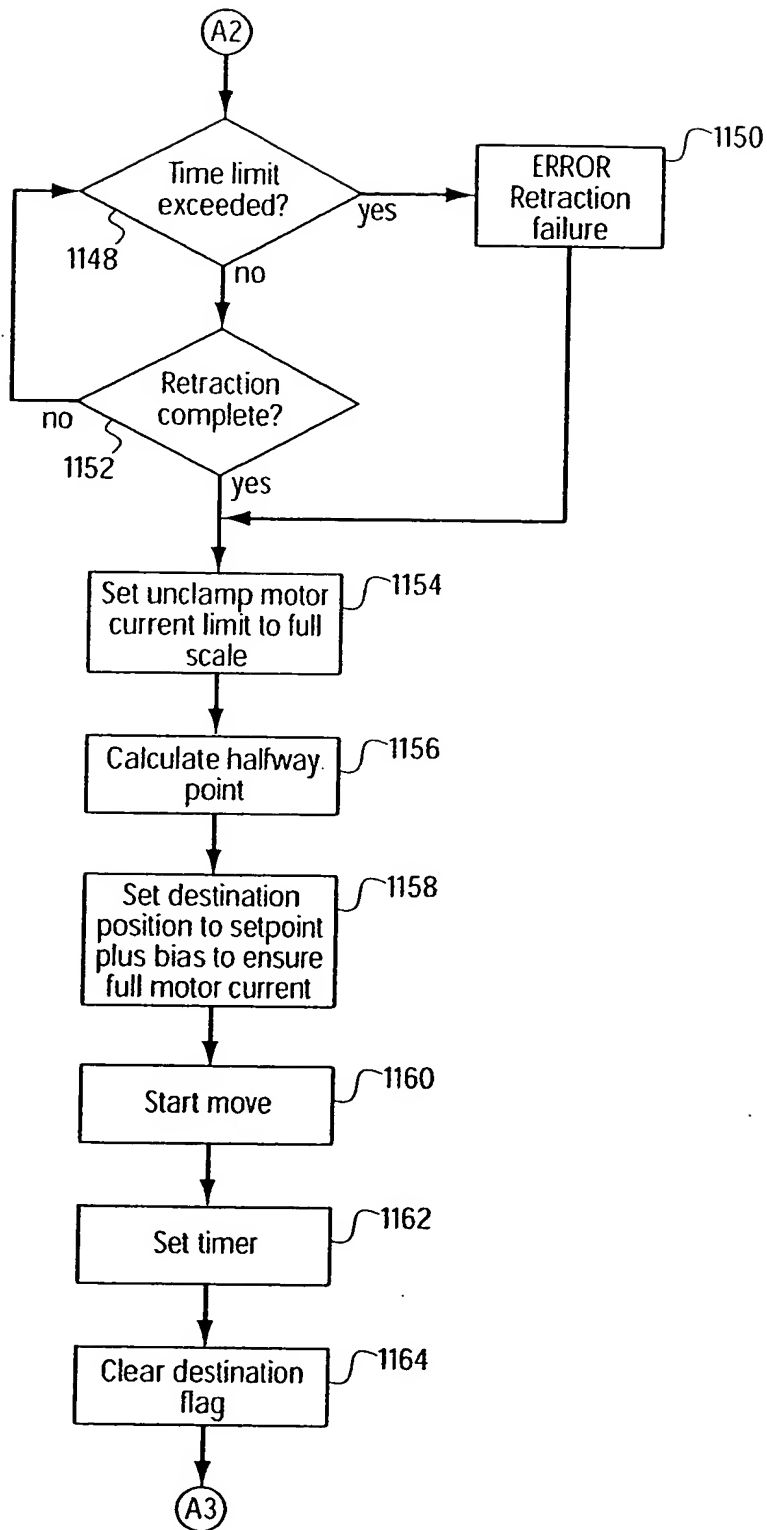


Fig. 14c

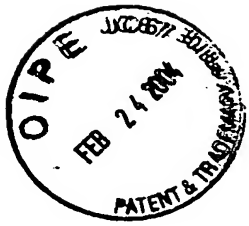


Fig. 14d

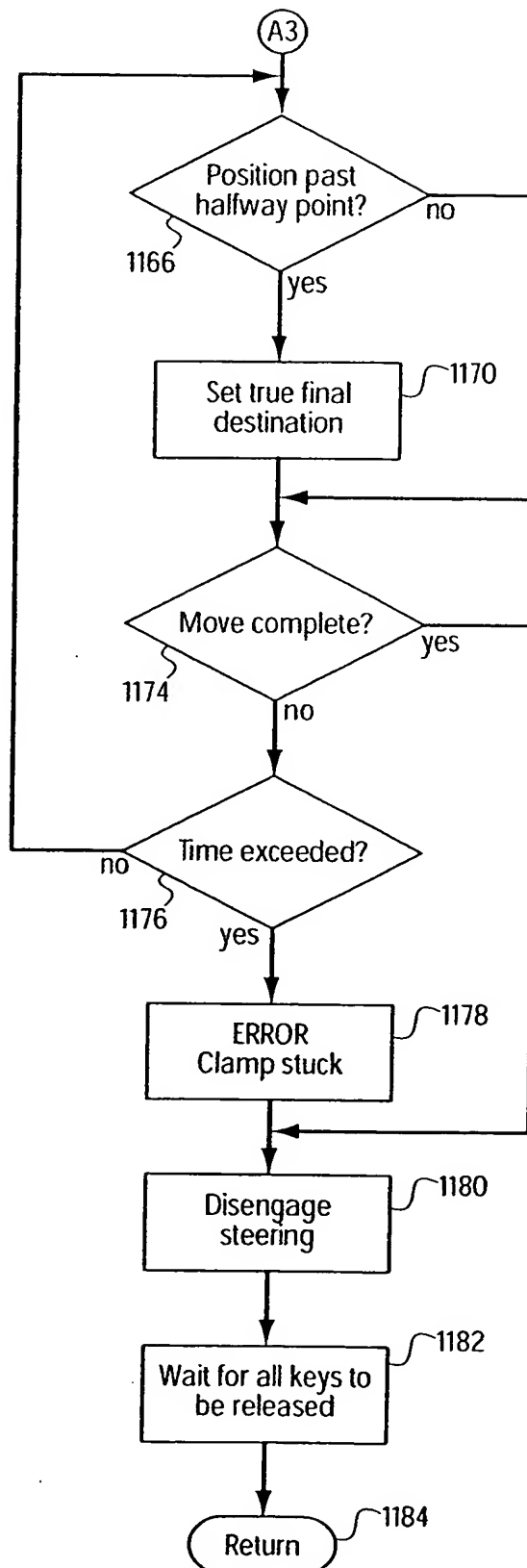


Fig.15a

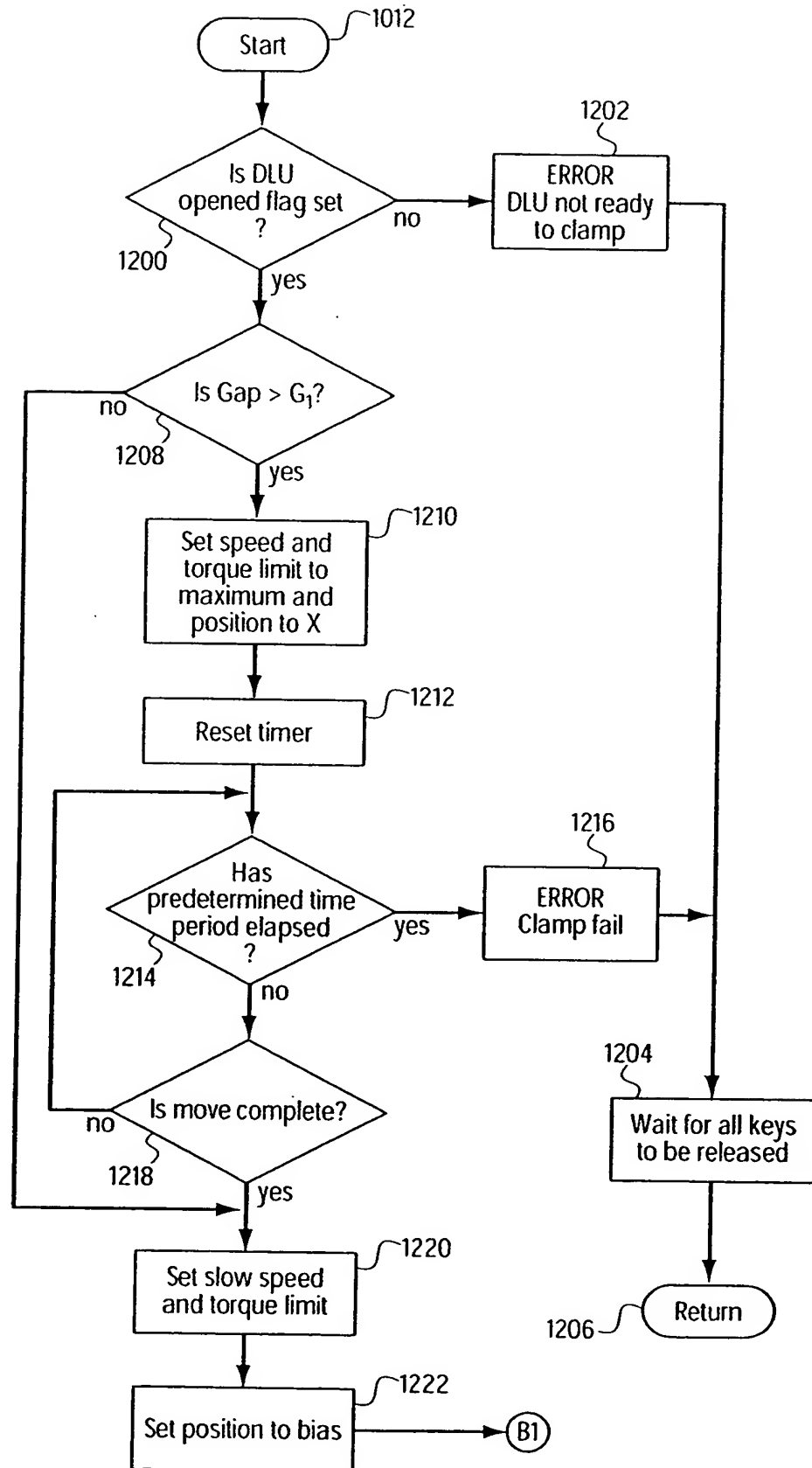
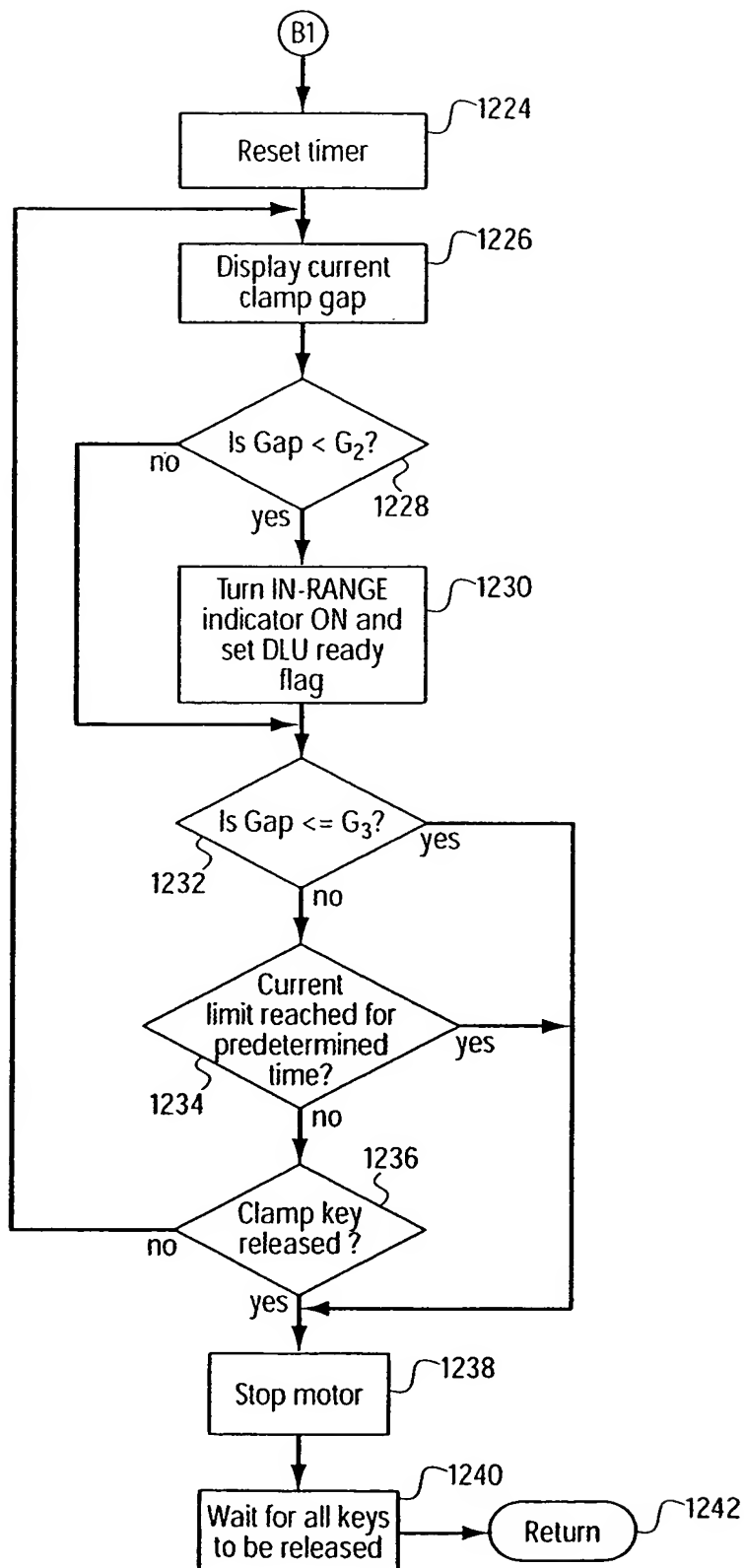


Fig.15b



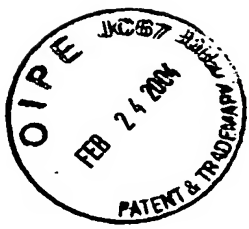
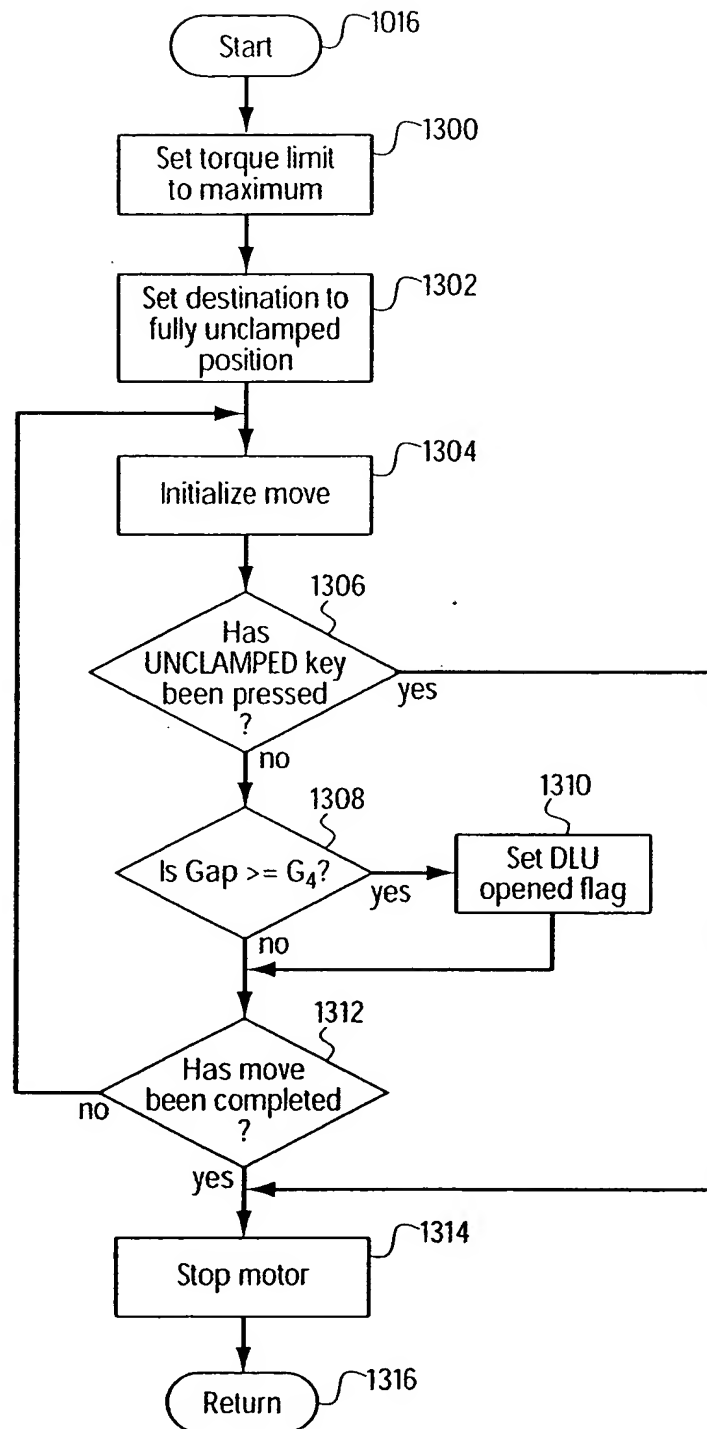


Fig.16



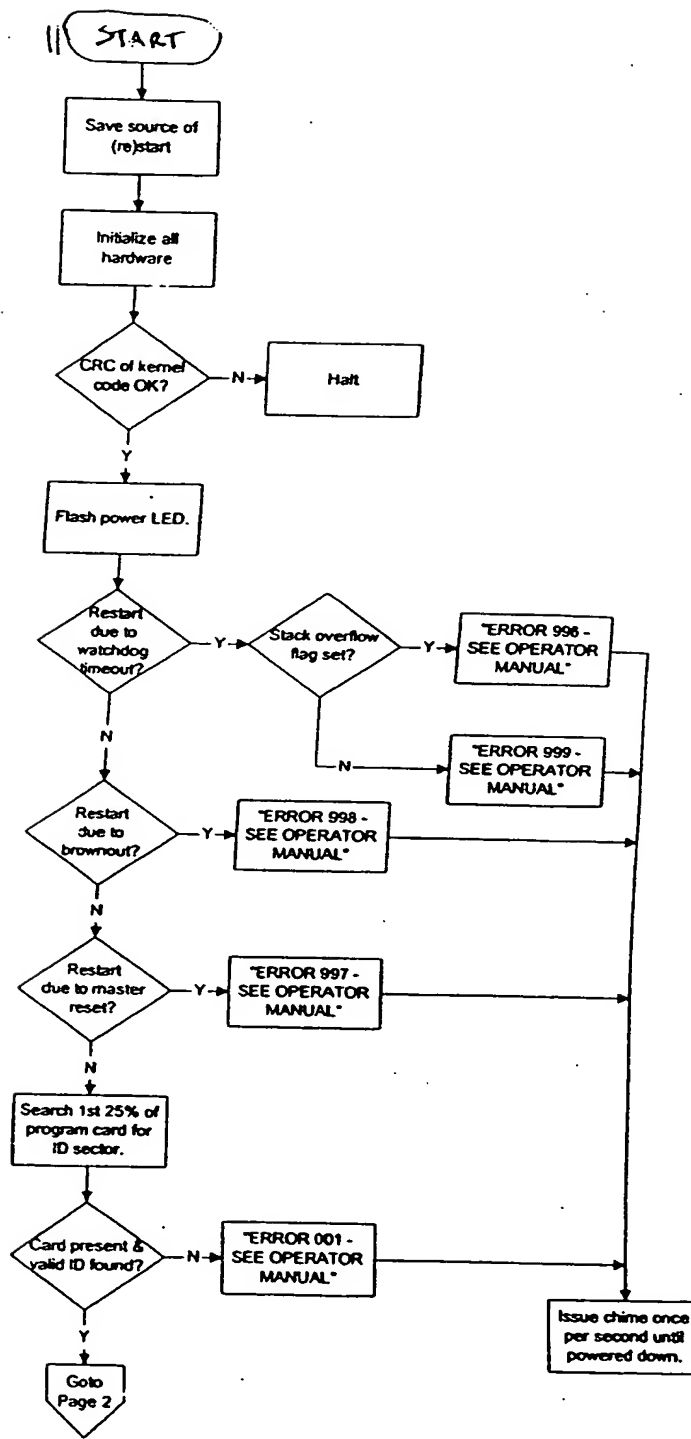
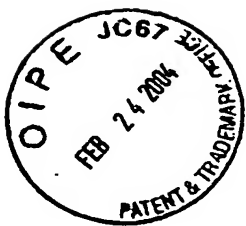


FIG. 17a

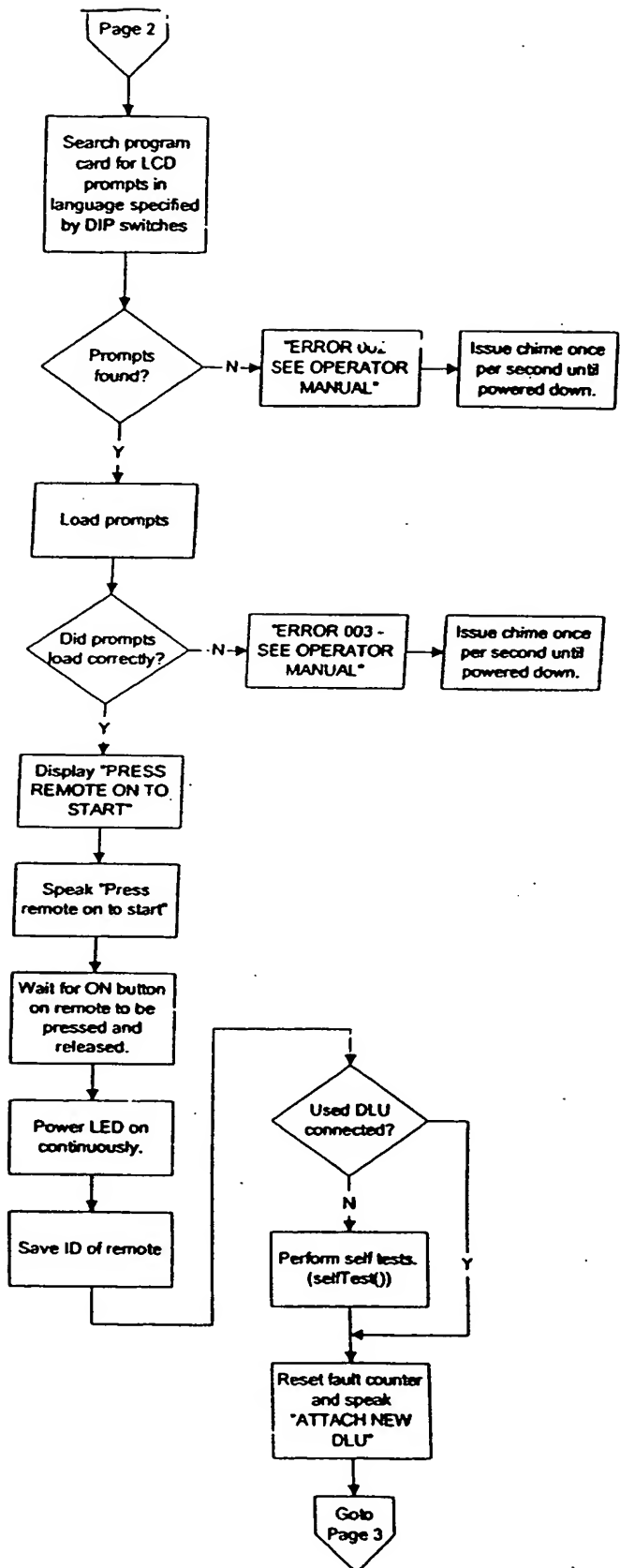
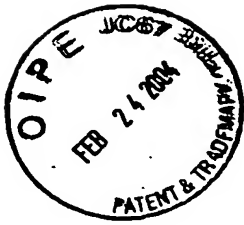


FIG. 17b



Main processing loop

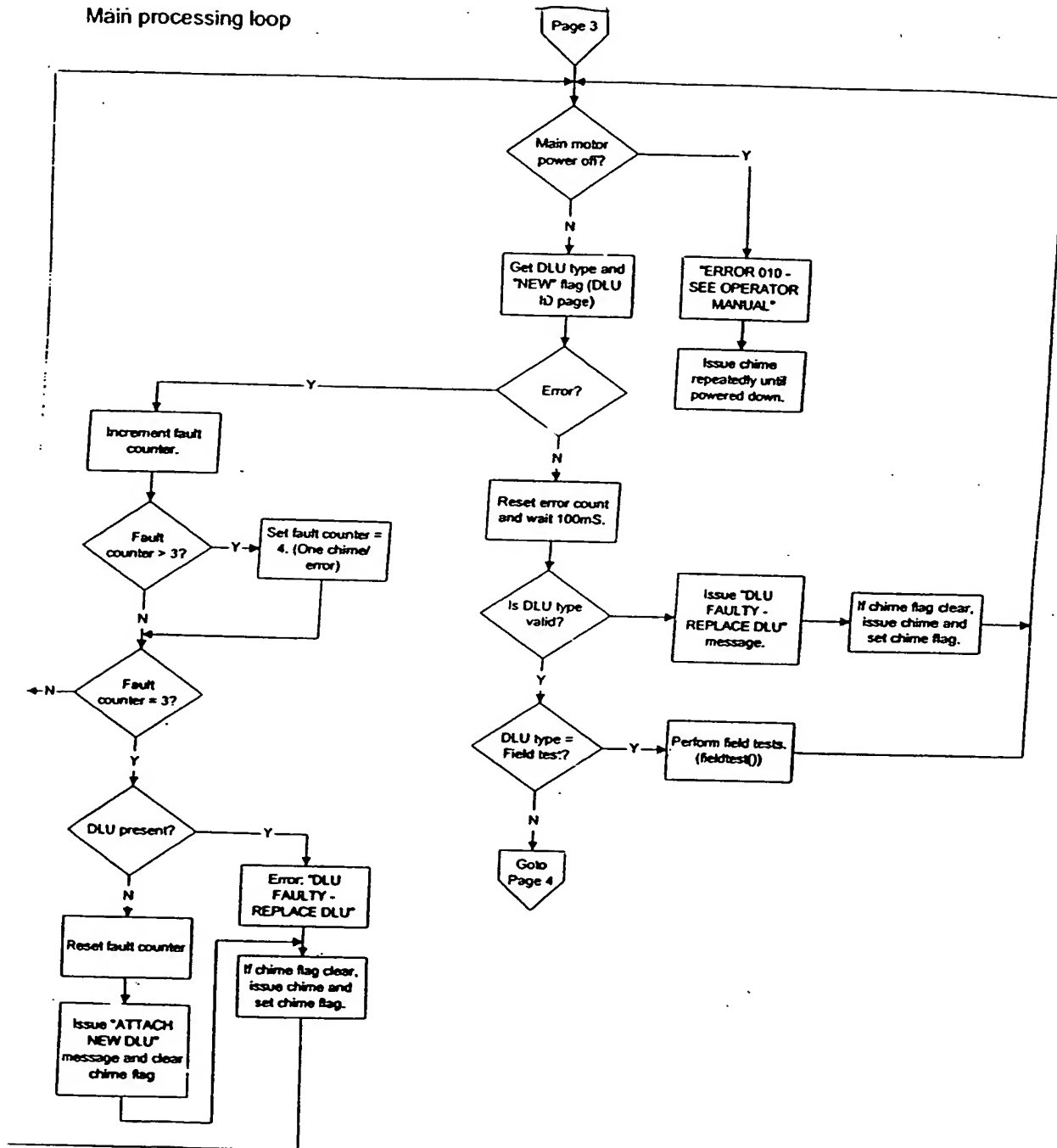


FIG. 17c

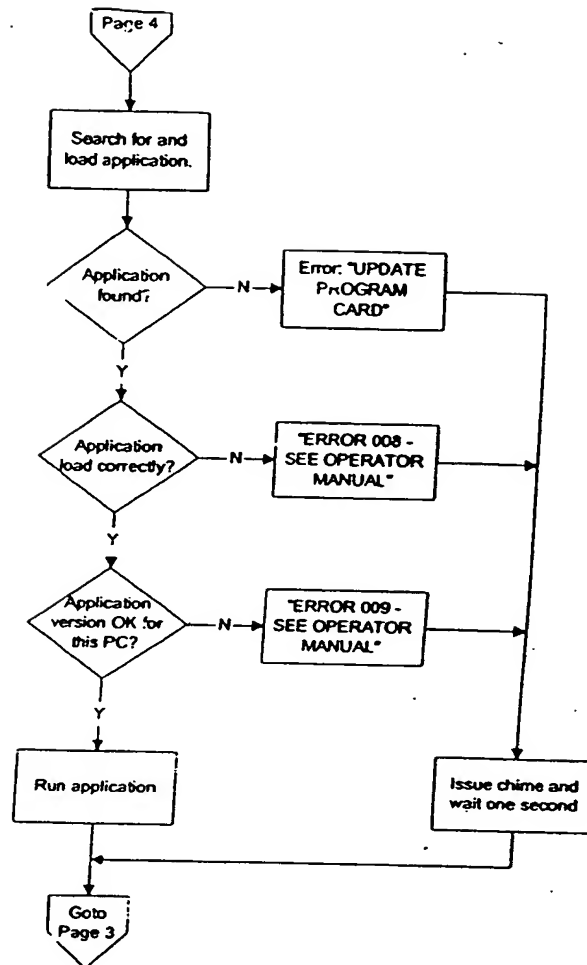
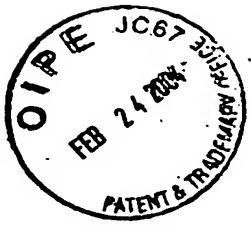


FIG. 17d

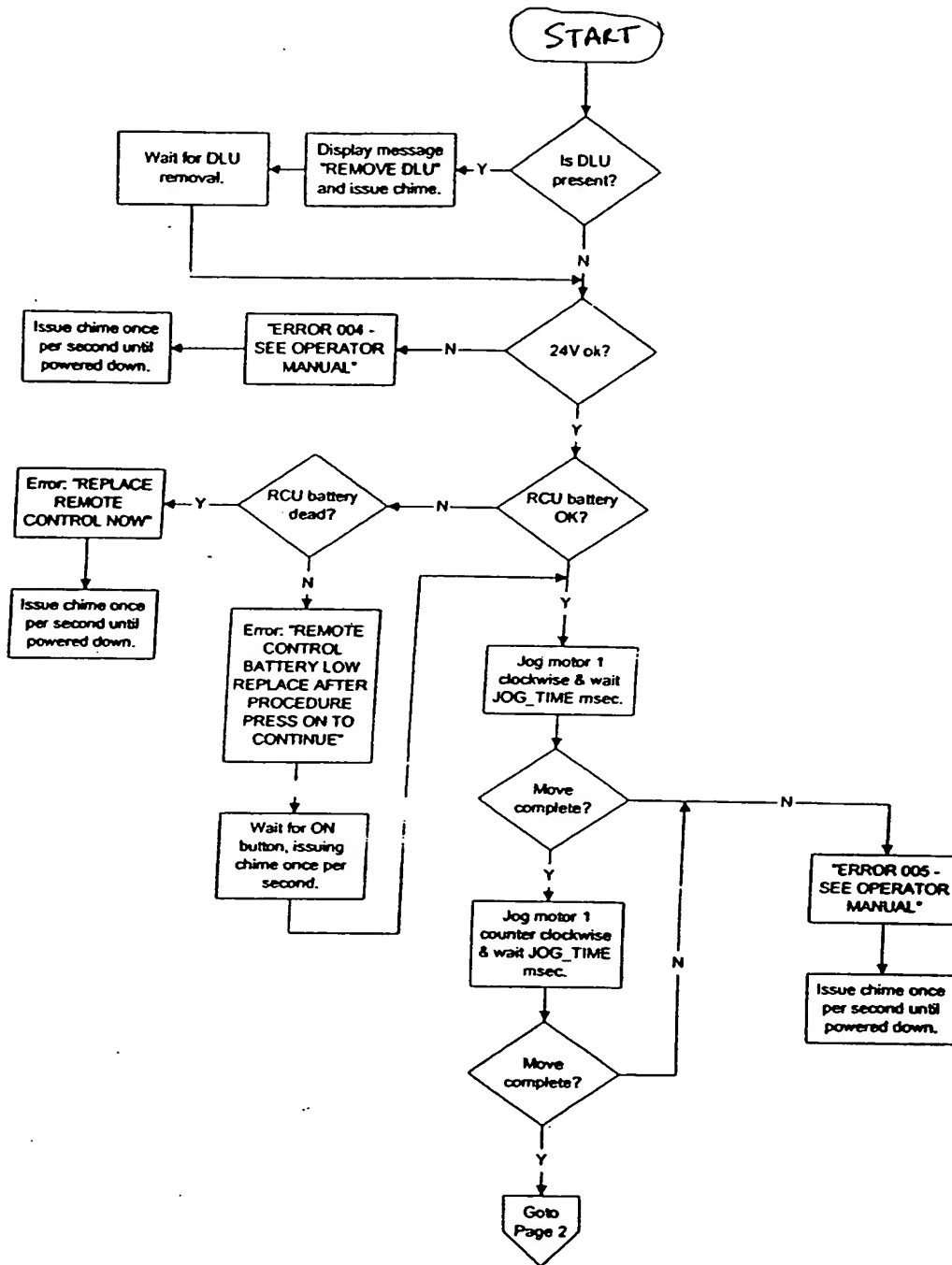


FIG. 18a

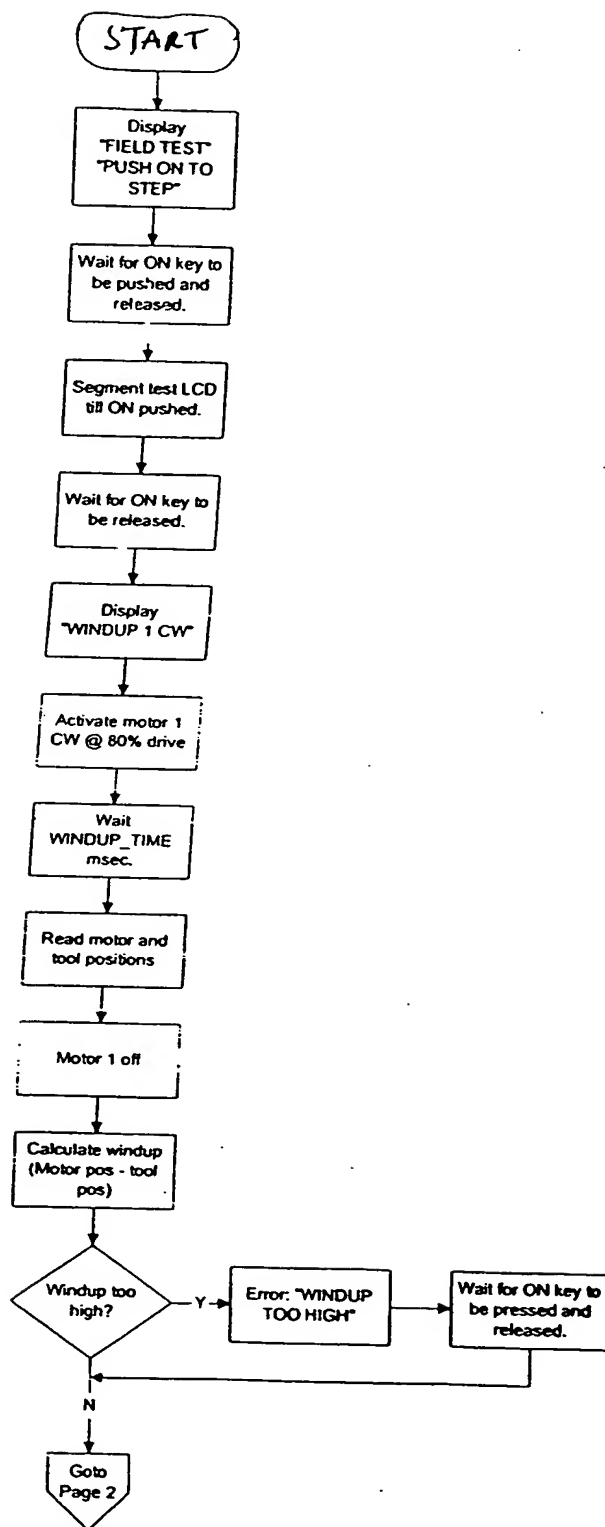


FIG. 19a

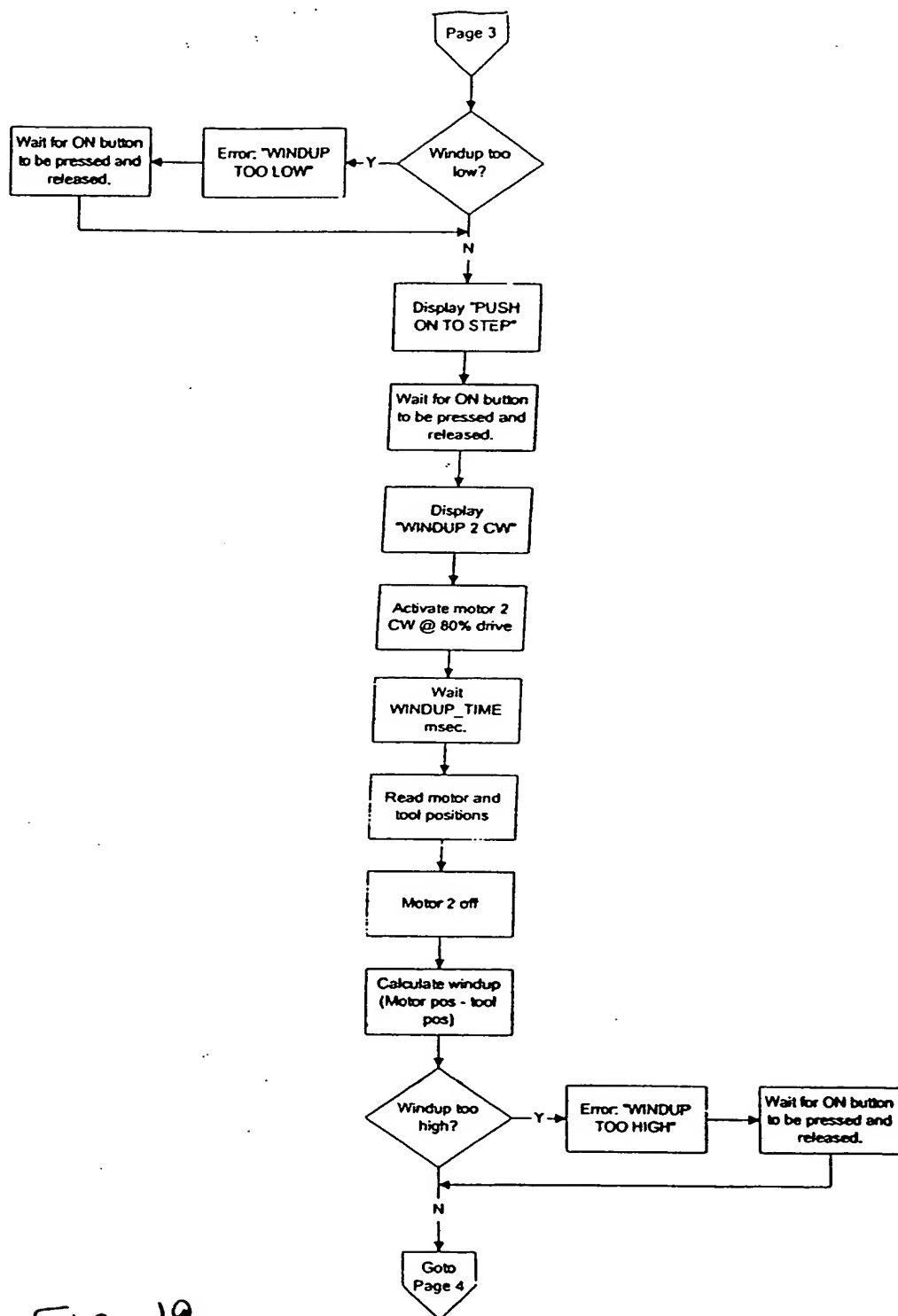


FIG. 19c

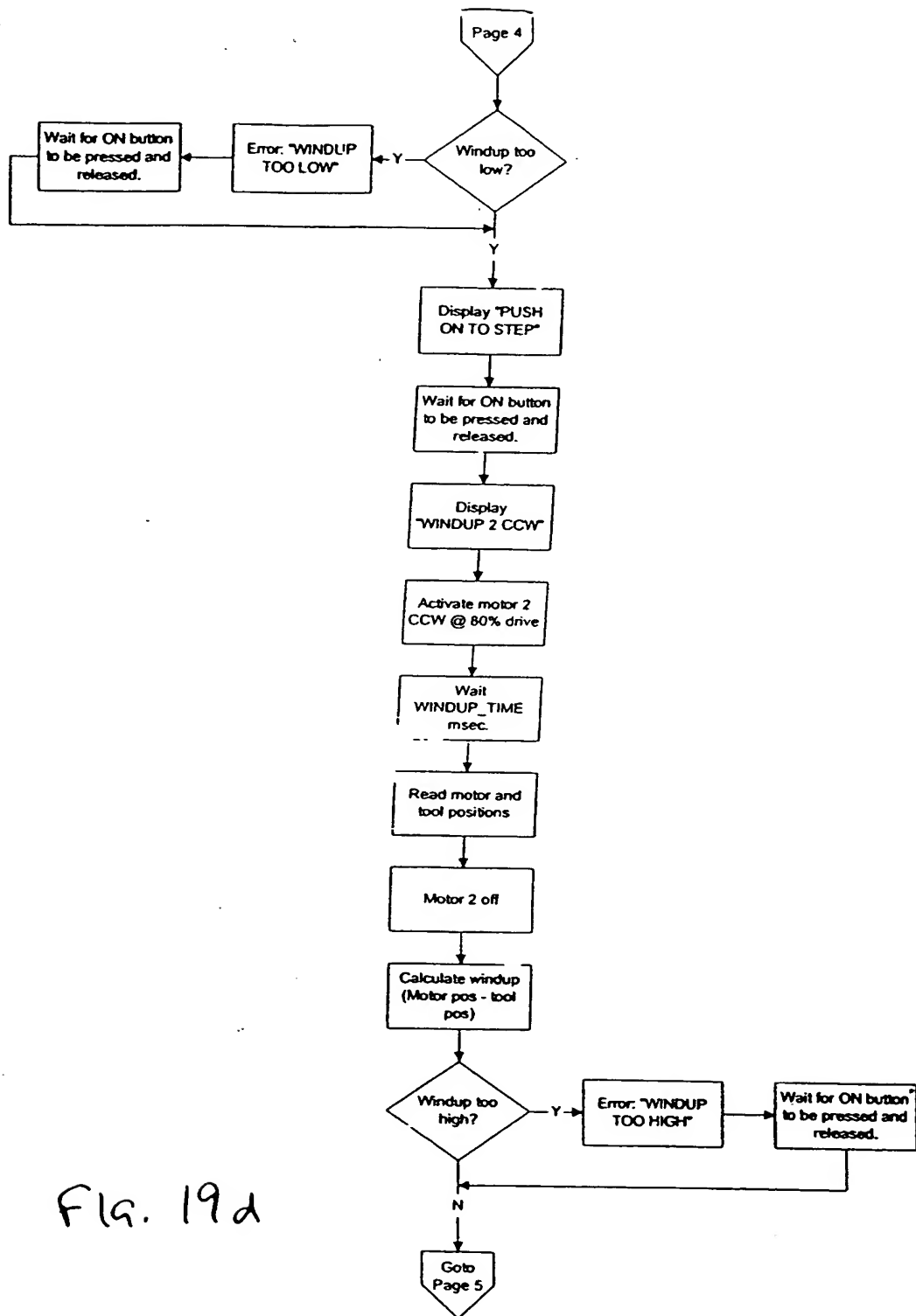


Fig. 19a

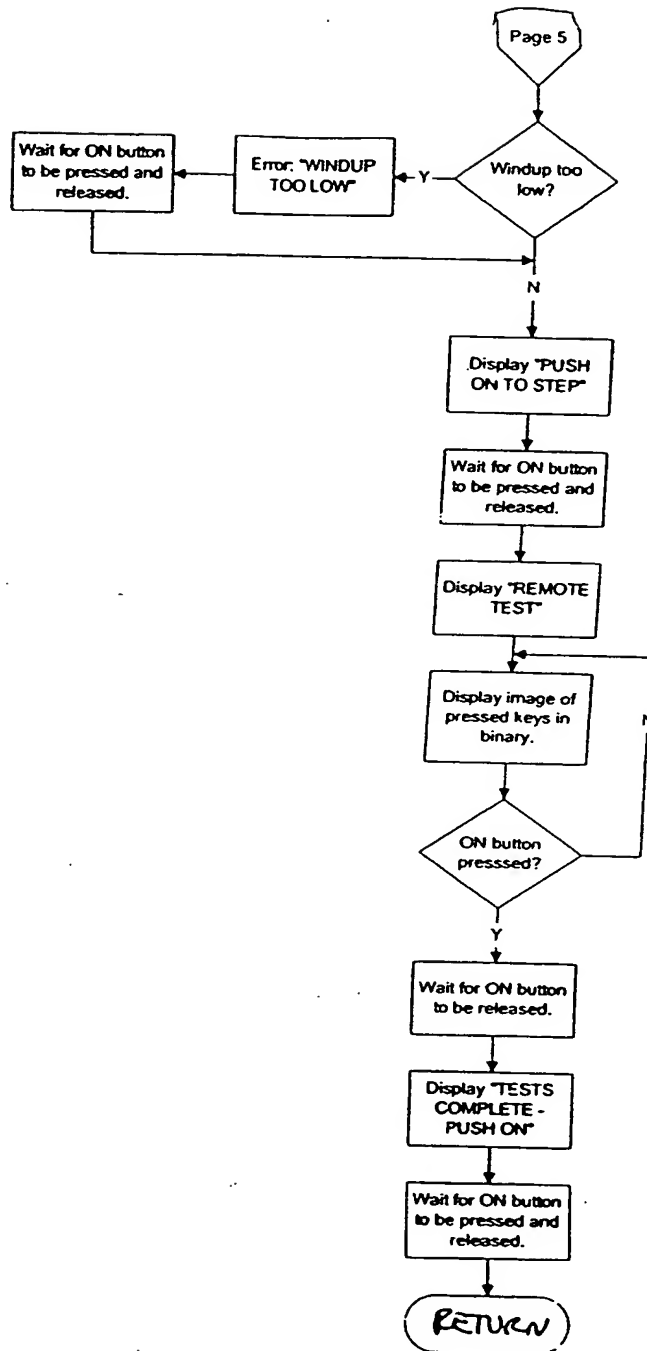


FIG. 19c

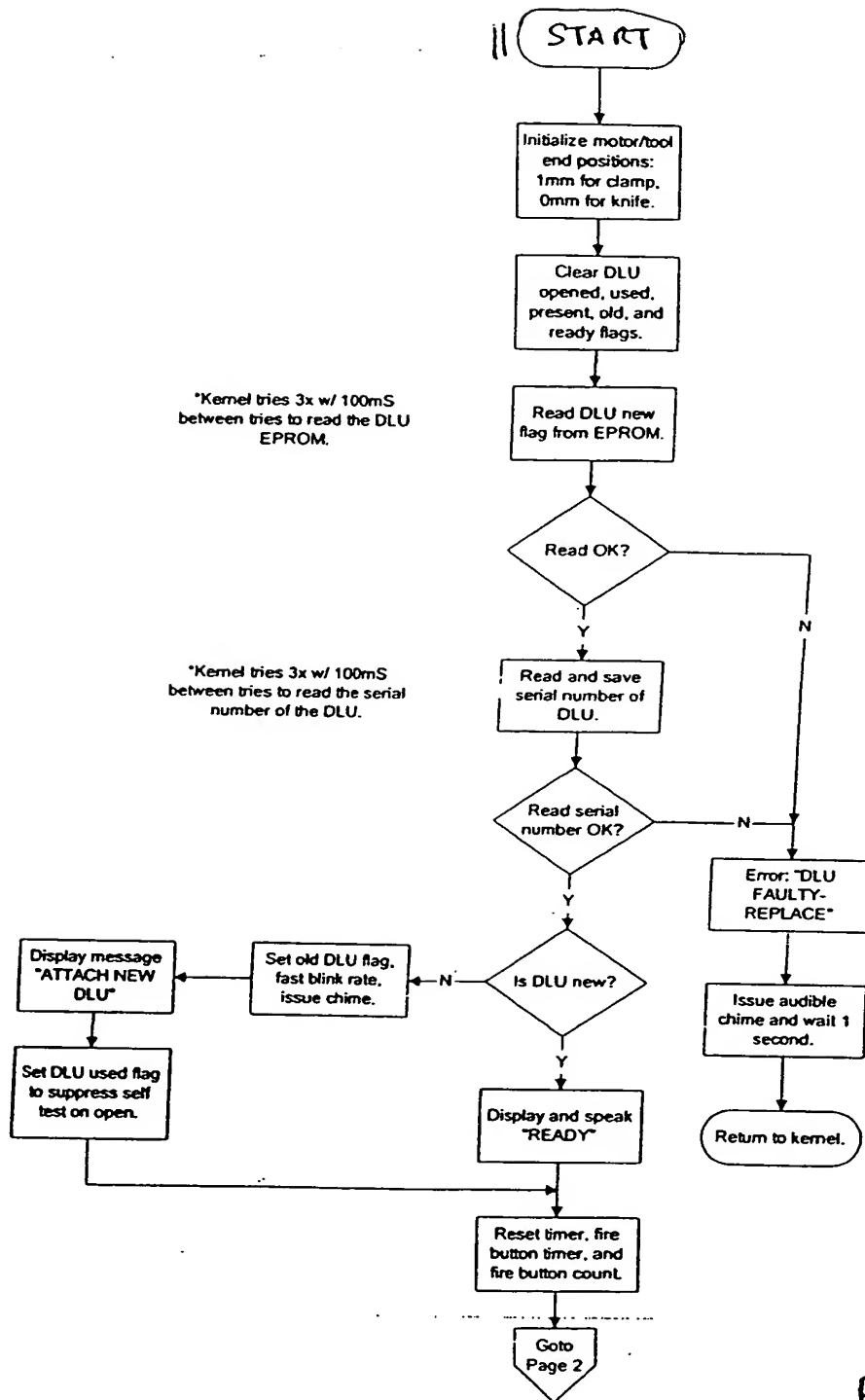


FIG. 20a

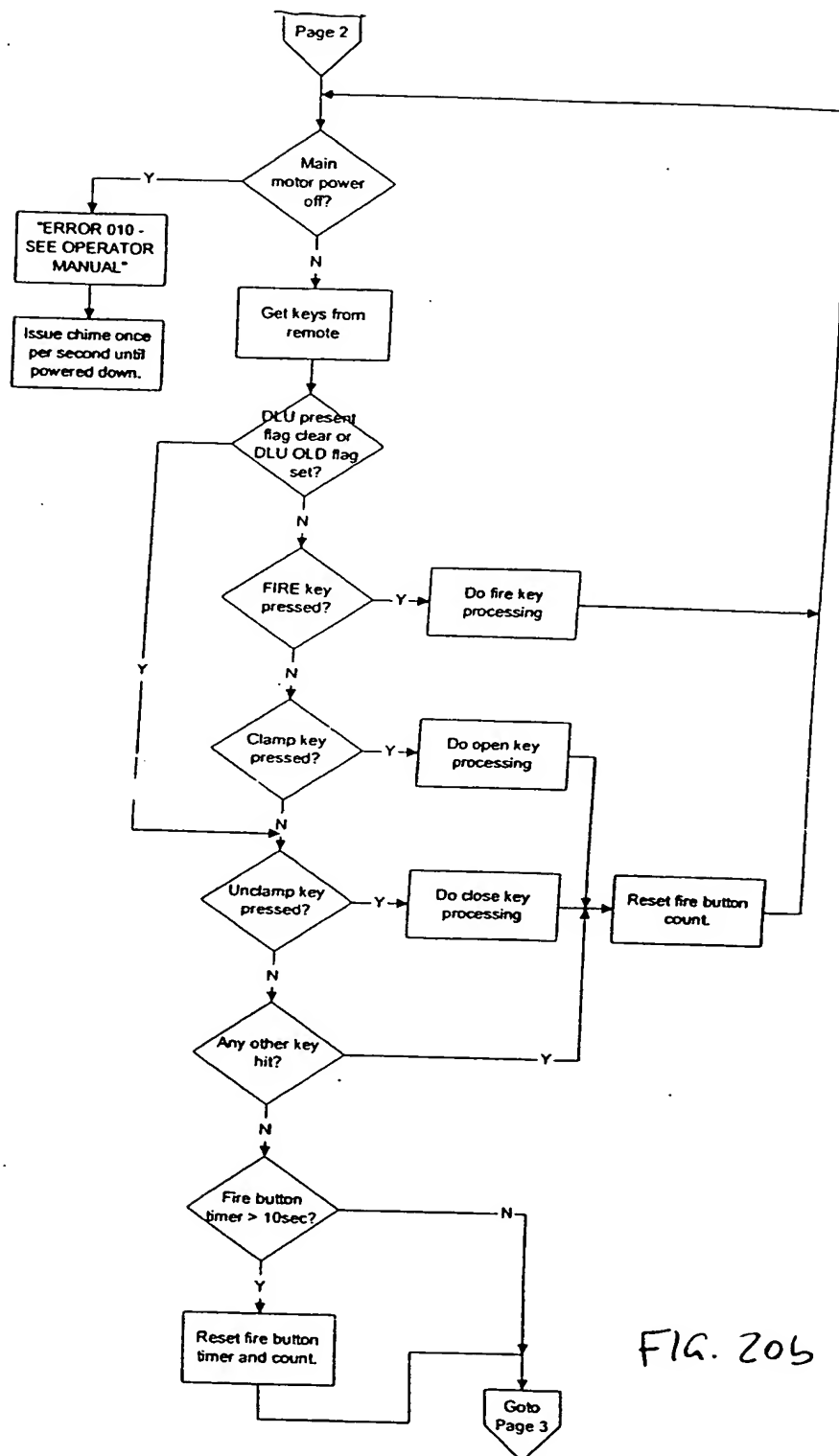
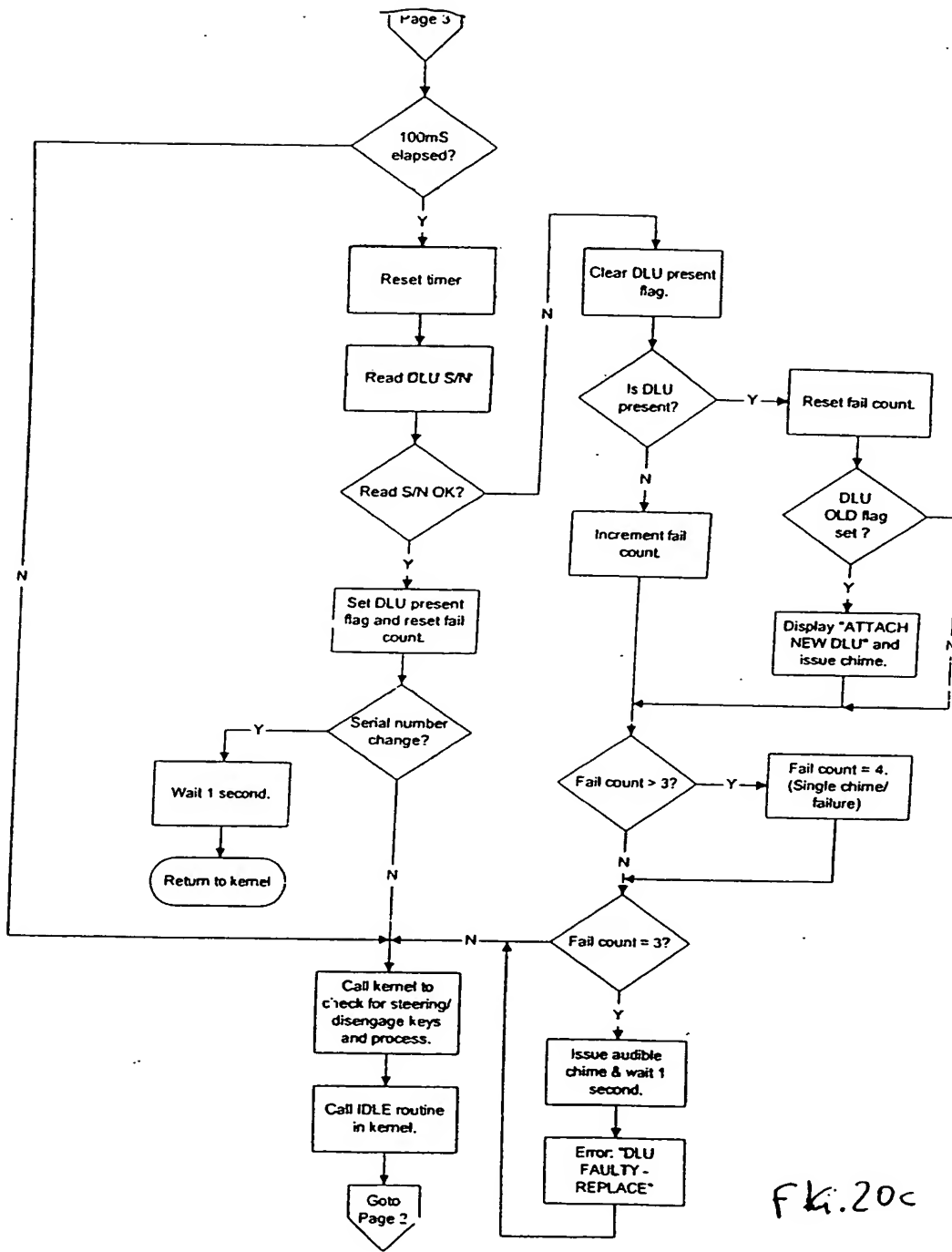


FIG. 205



Fk.20c

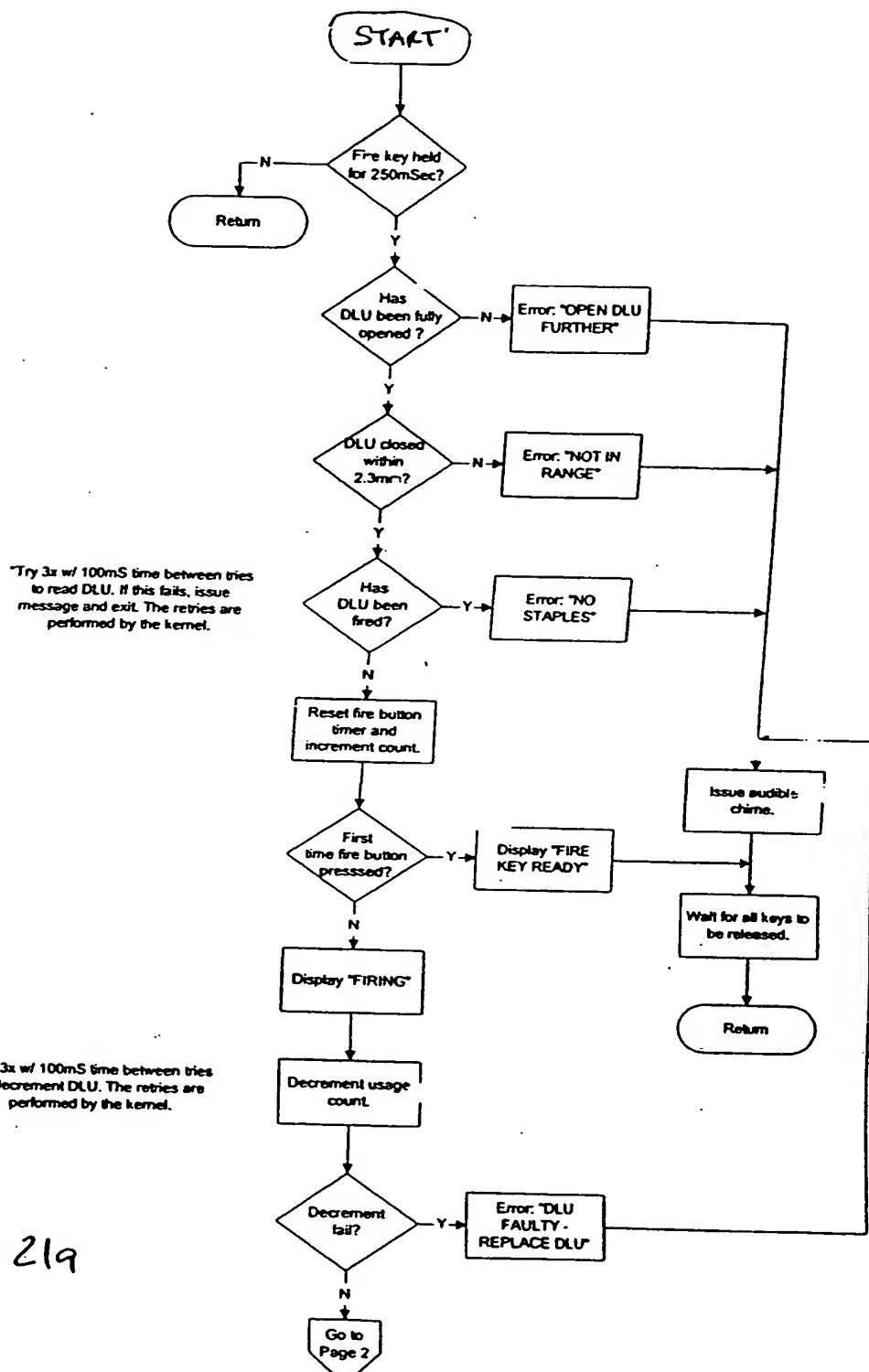
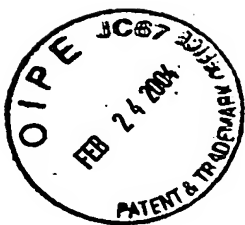


FIG. 21a

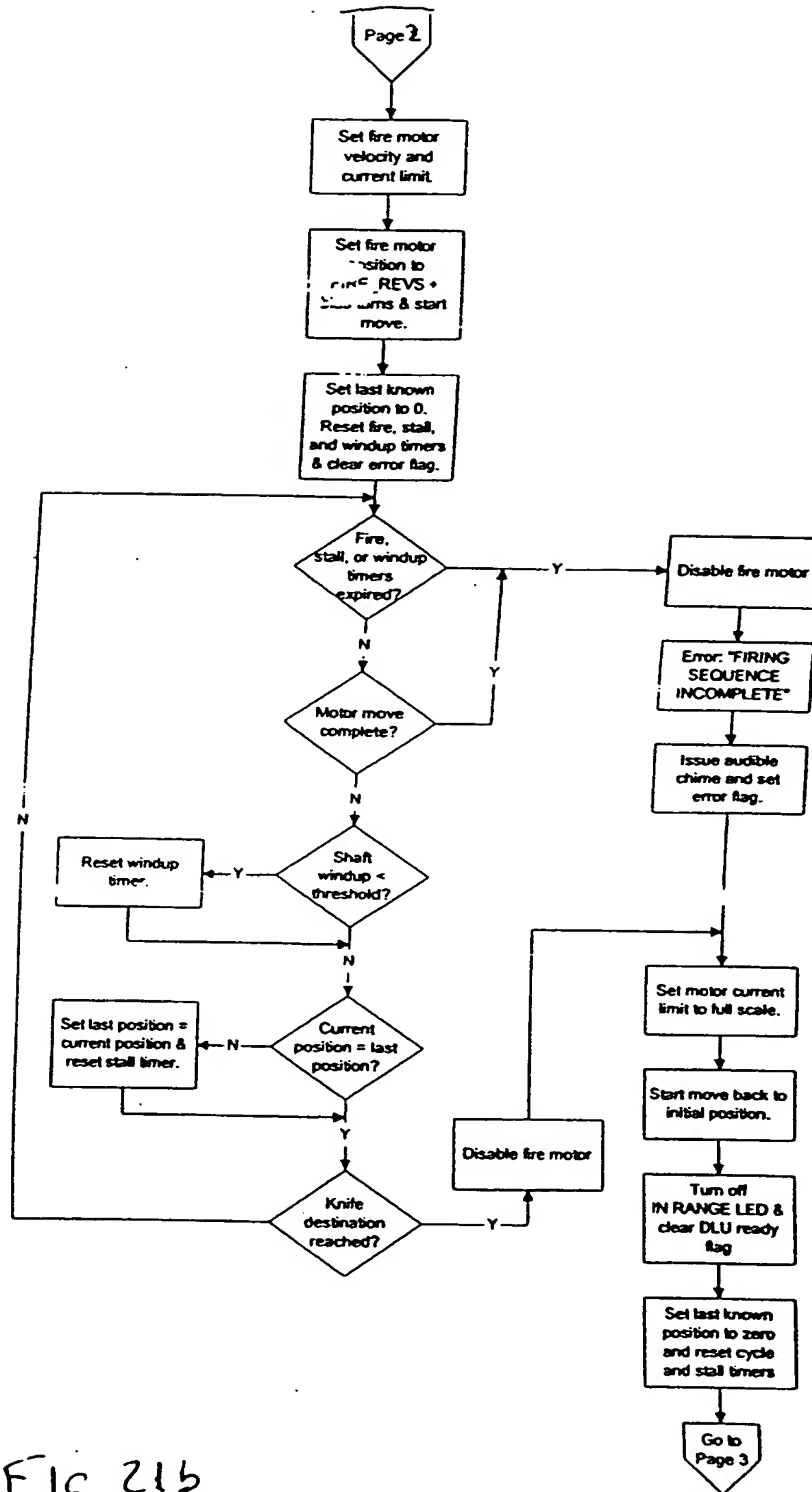


FIG. 21b

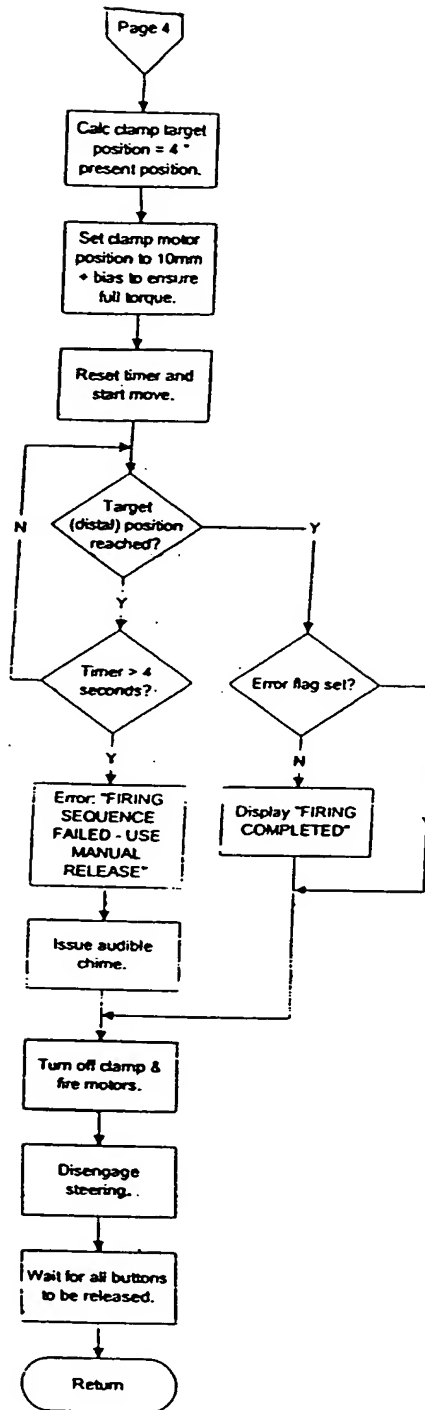
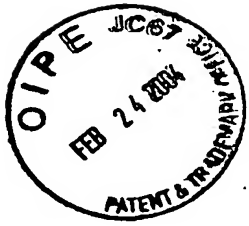


FIG. 21d

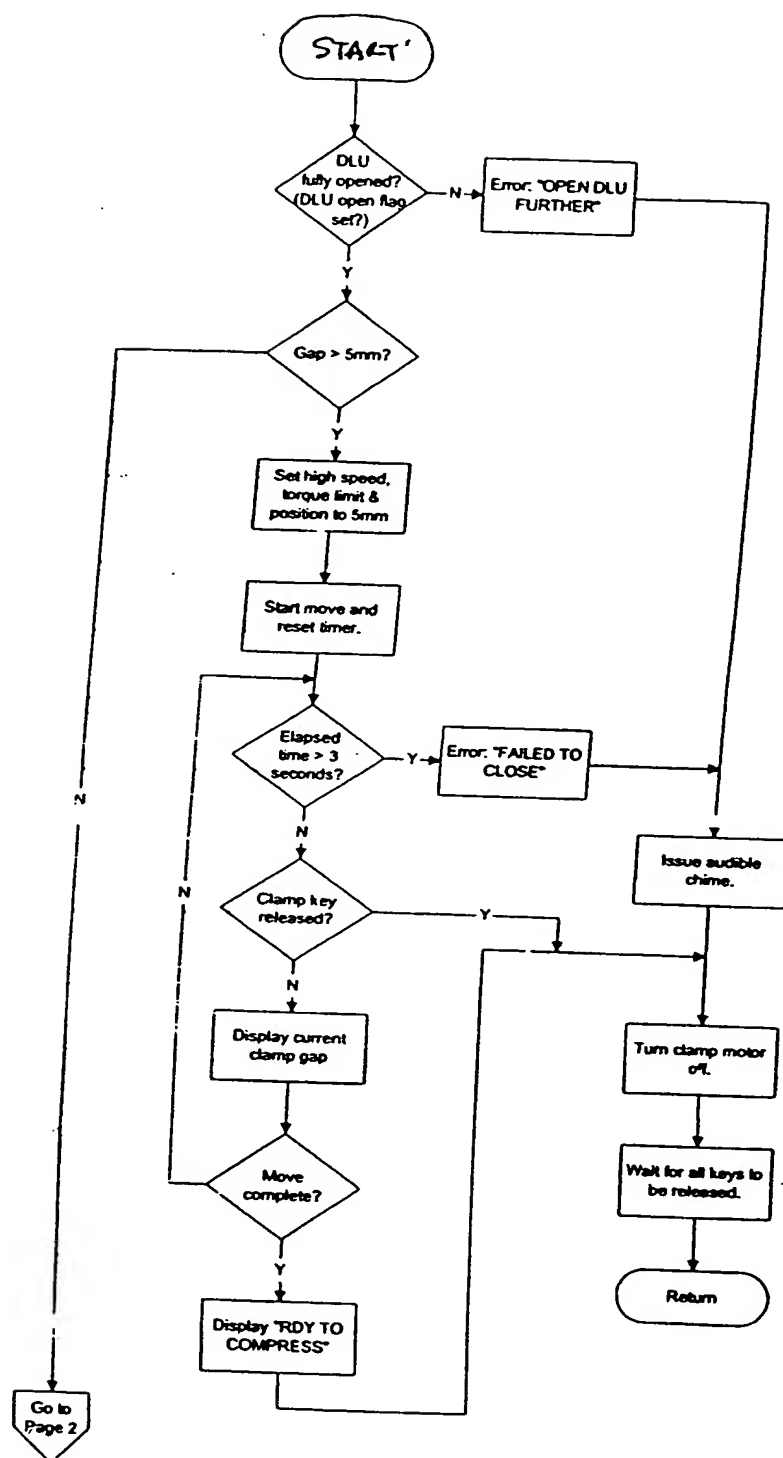


FIG. 22a

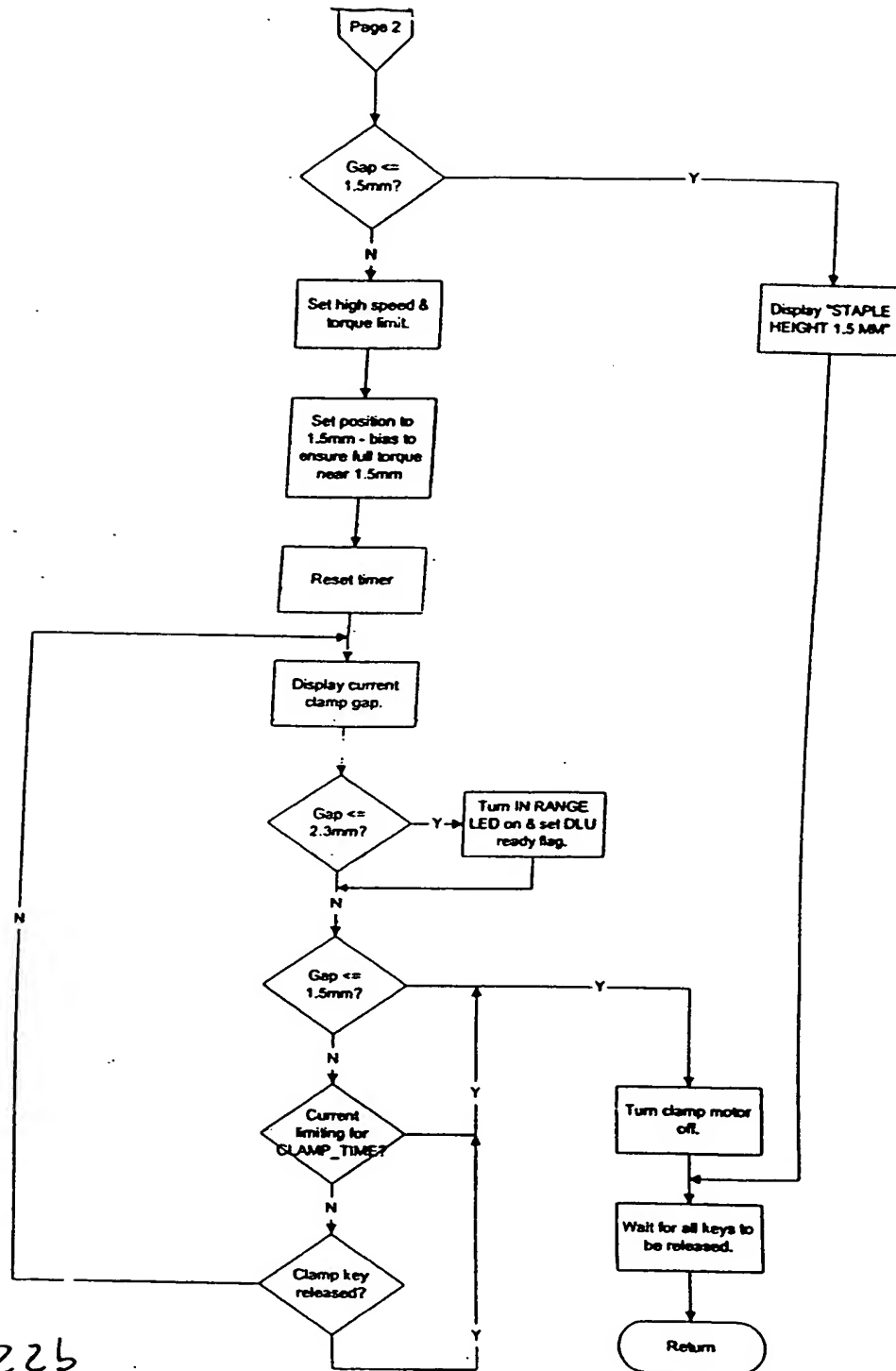


FIG. 22b



*The kernel tries 3x w/ 100mS time between tries to write DLU.

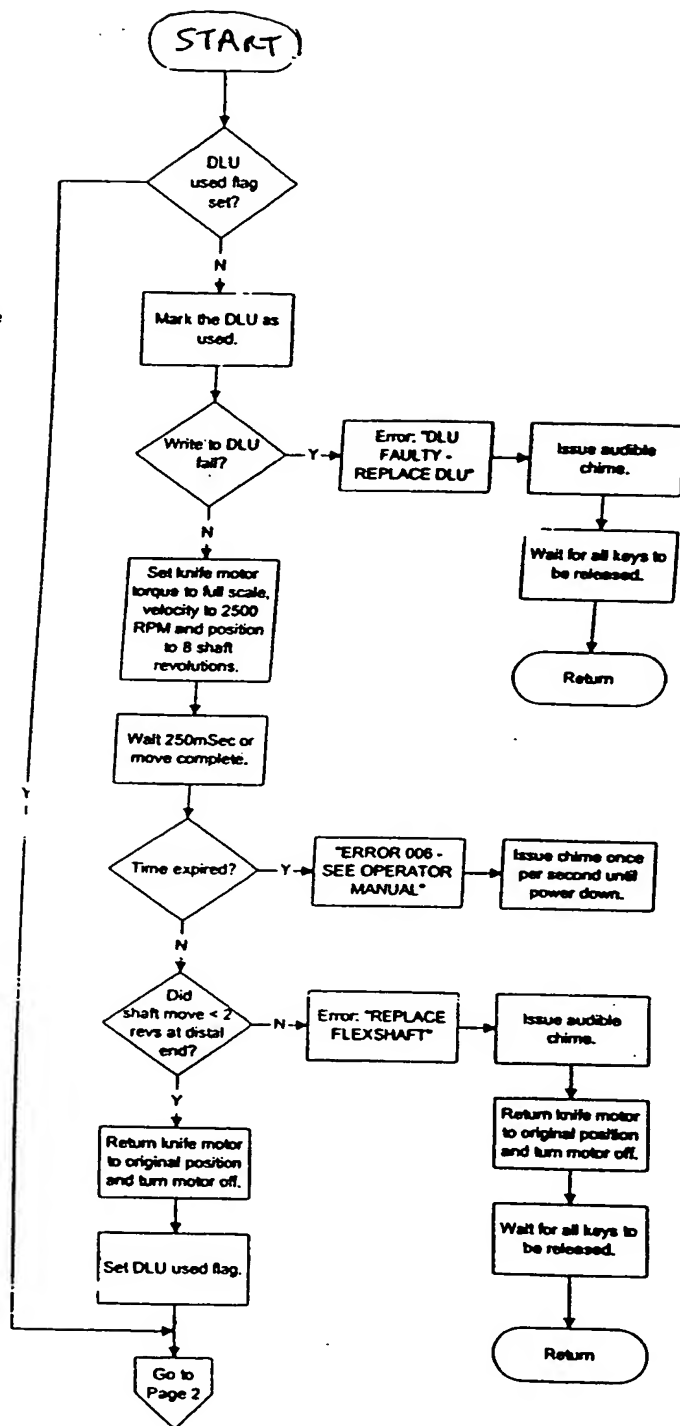


FIG. 23a

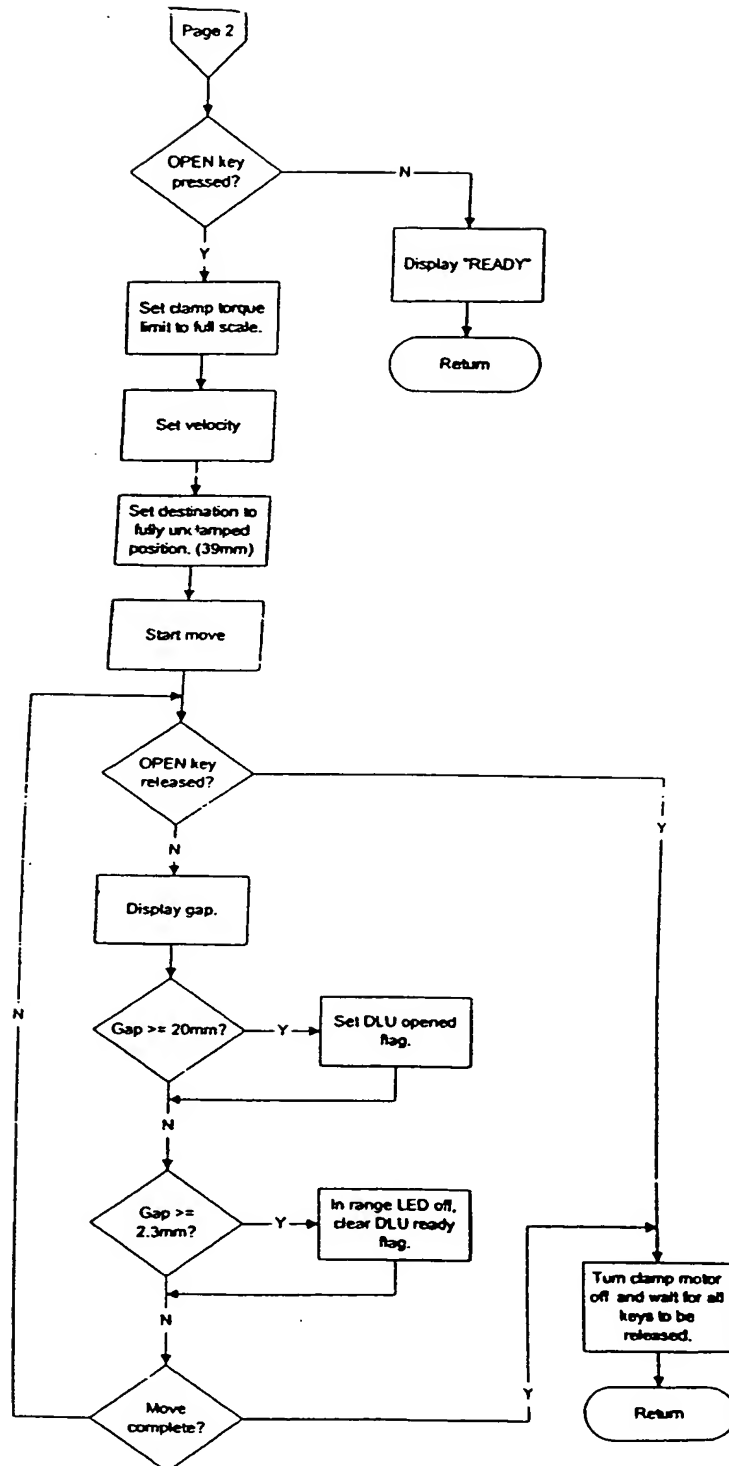


FIG. 23b

Figure 24a

500 →

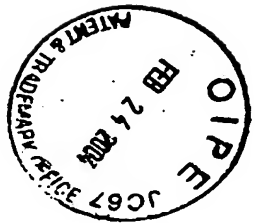
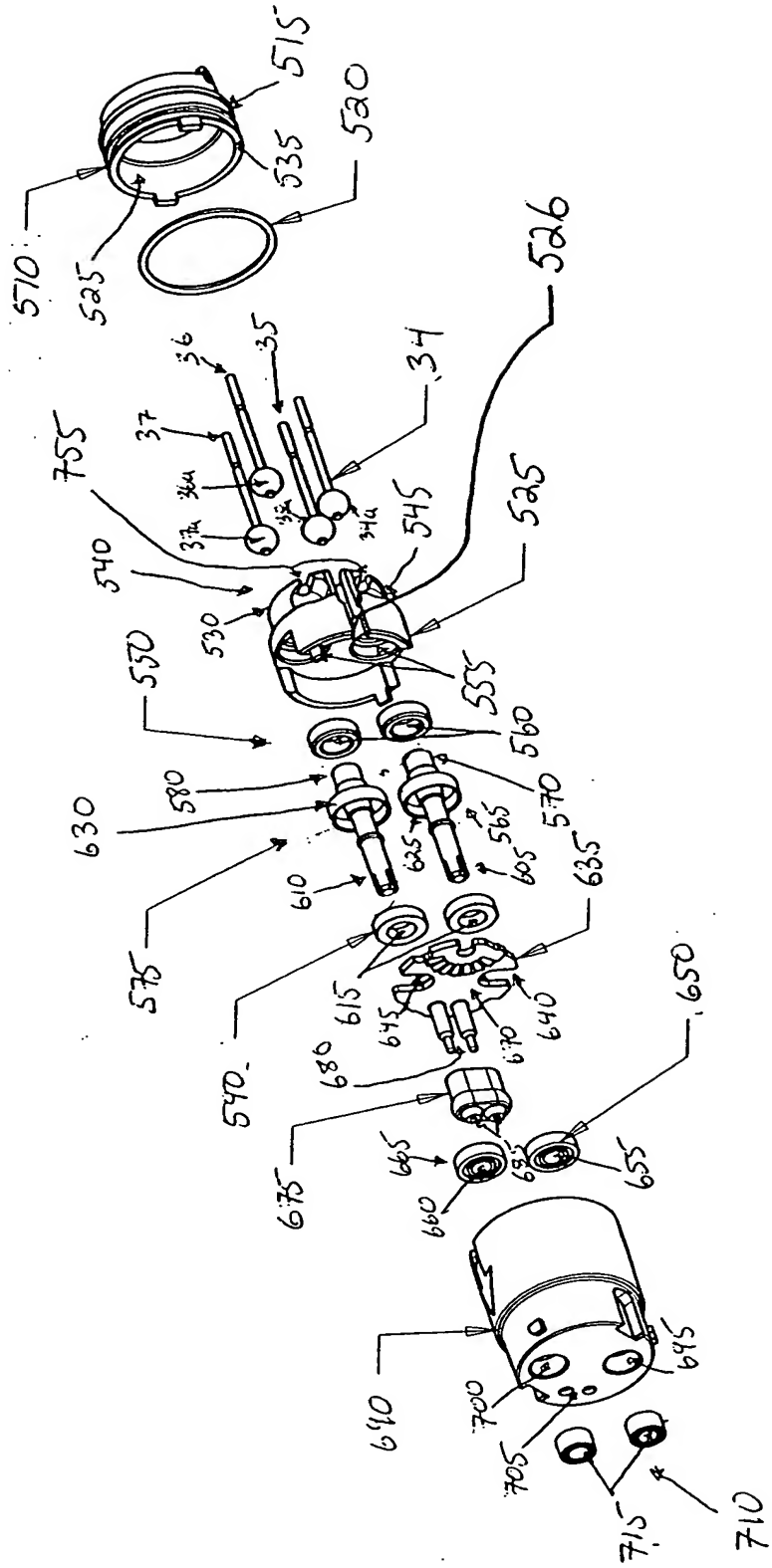




Figure 24b

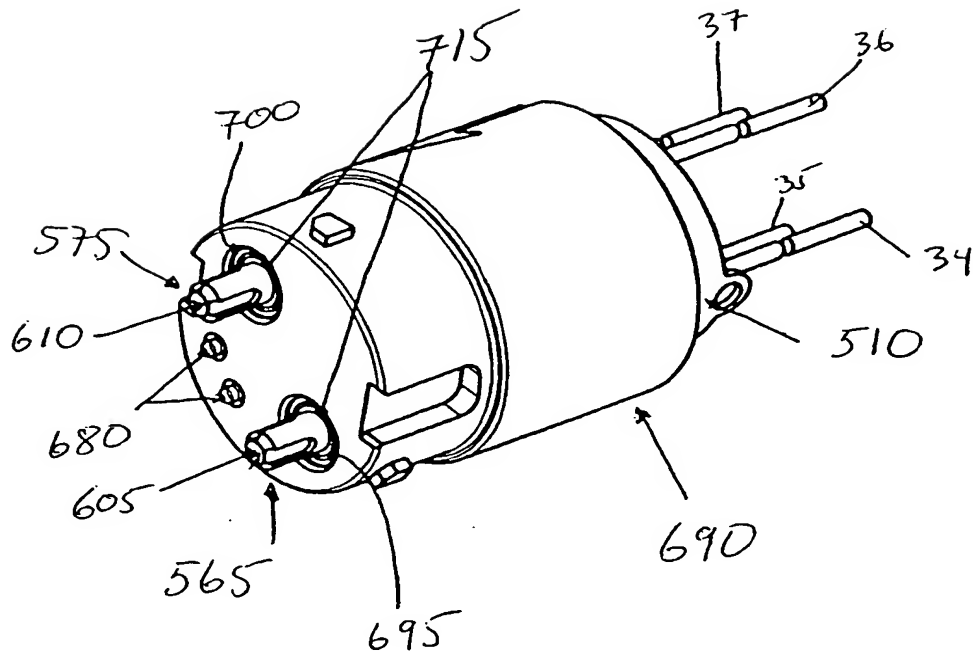


Figure 25

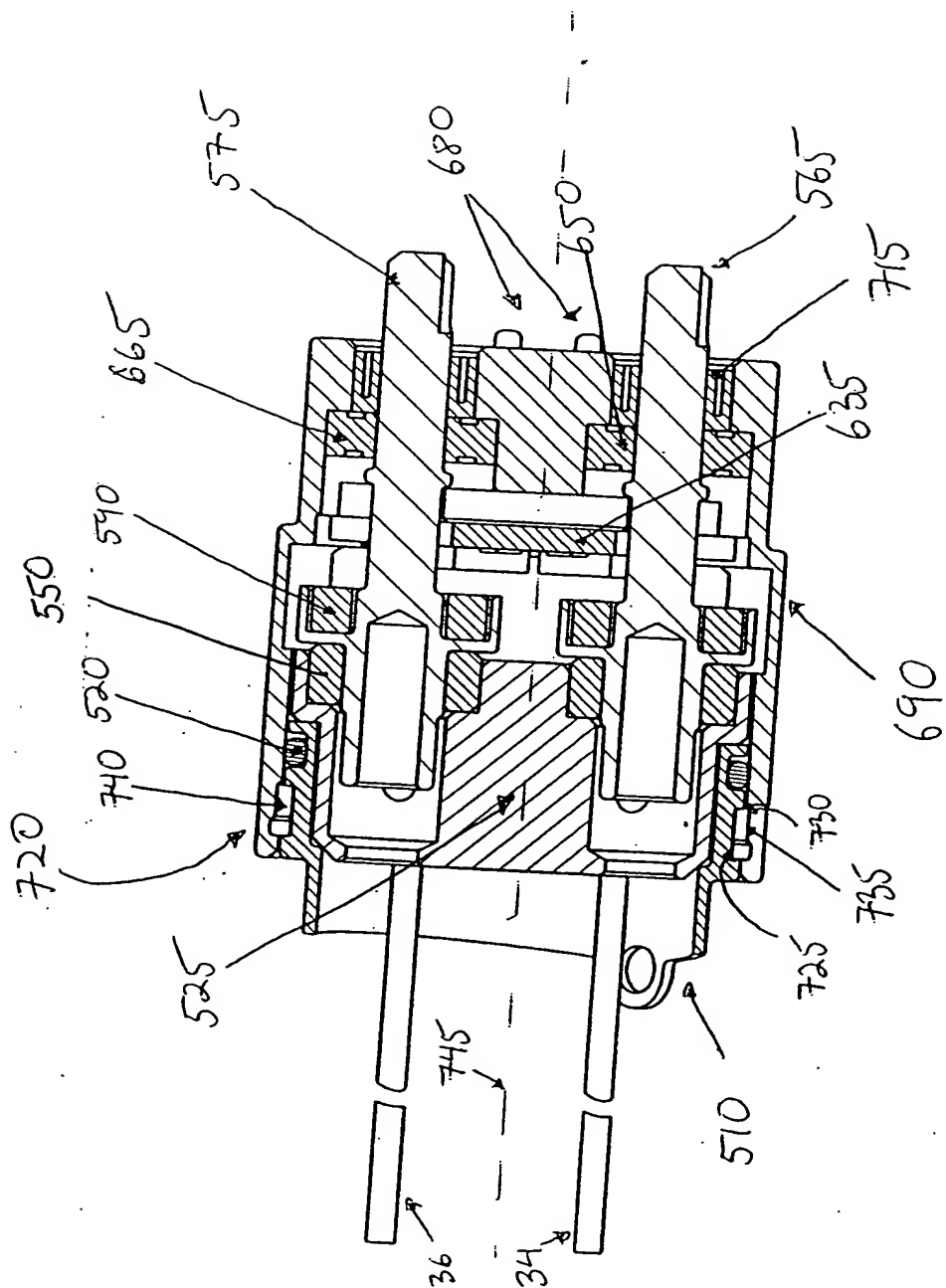




Figure 26a

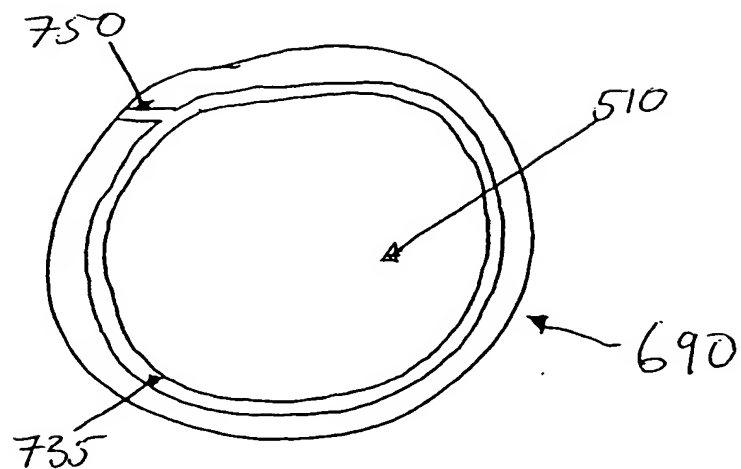


Figure 26b

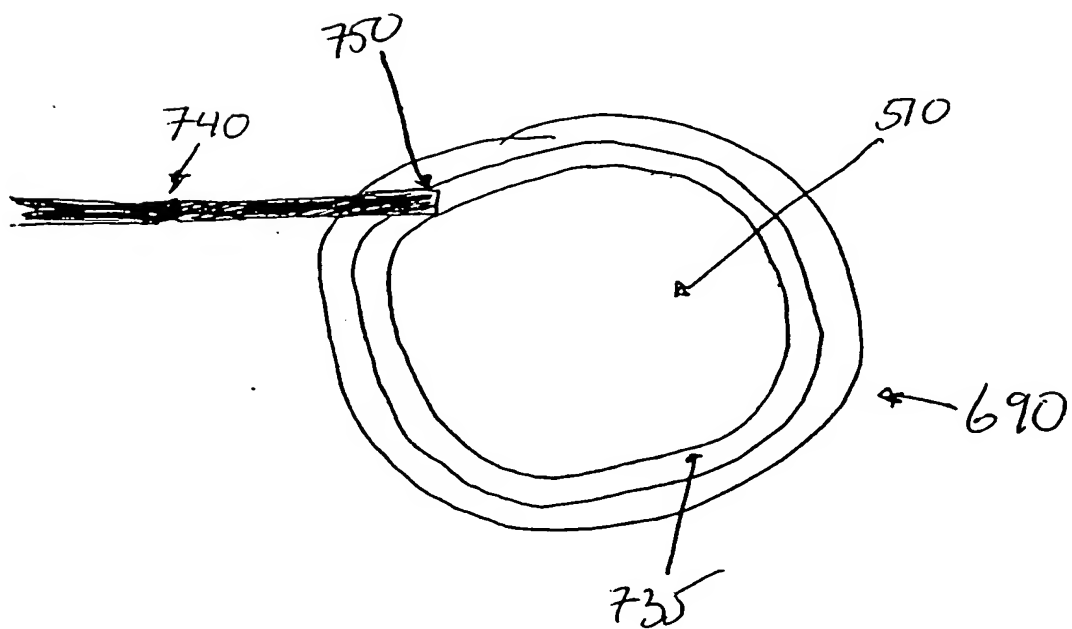




Figure 26c

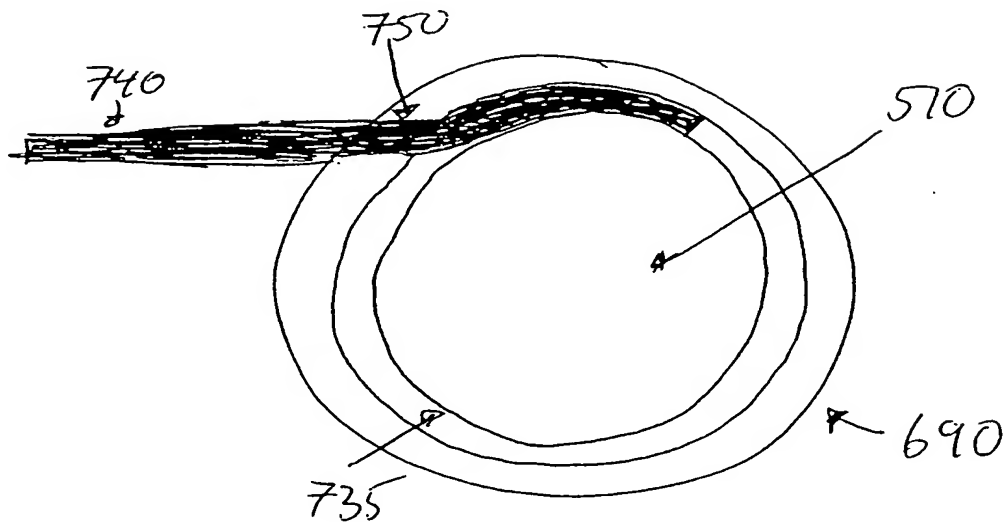


Figure 26d

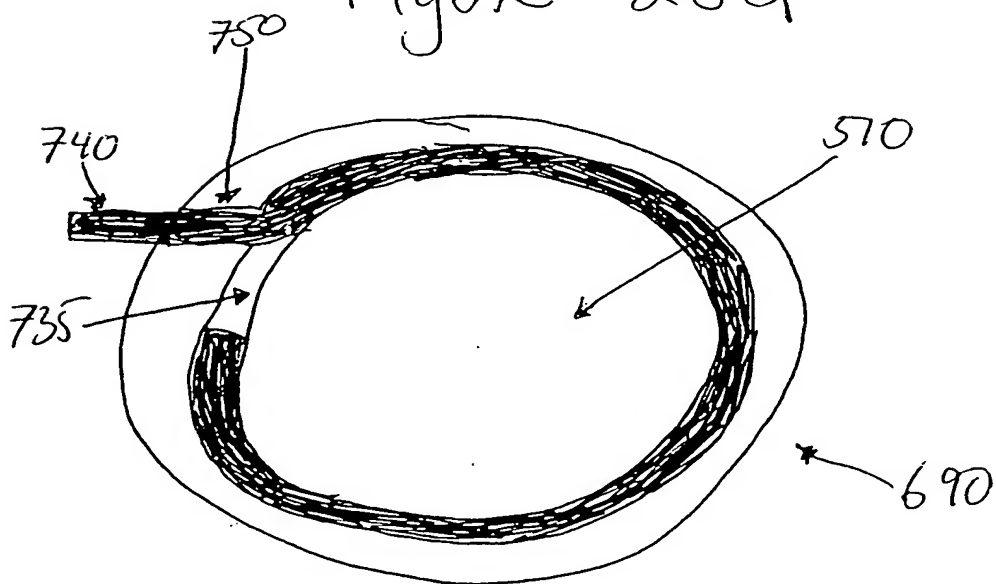


Figure 27

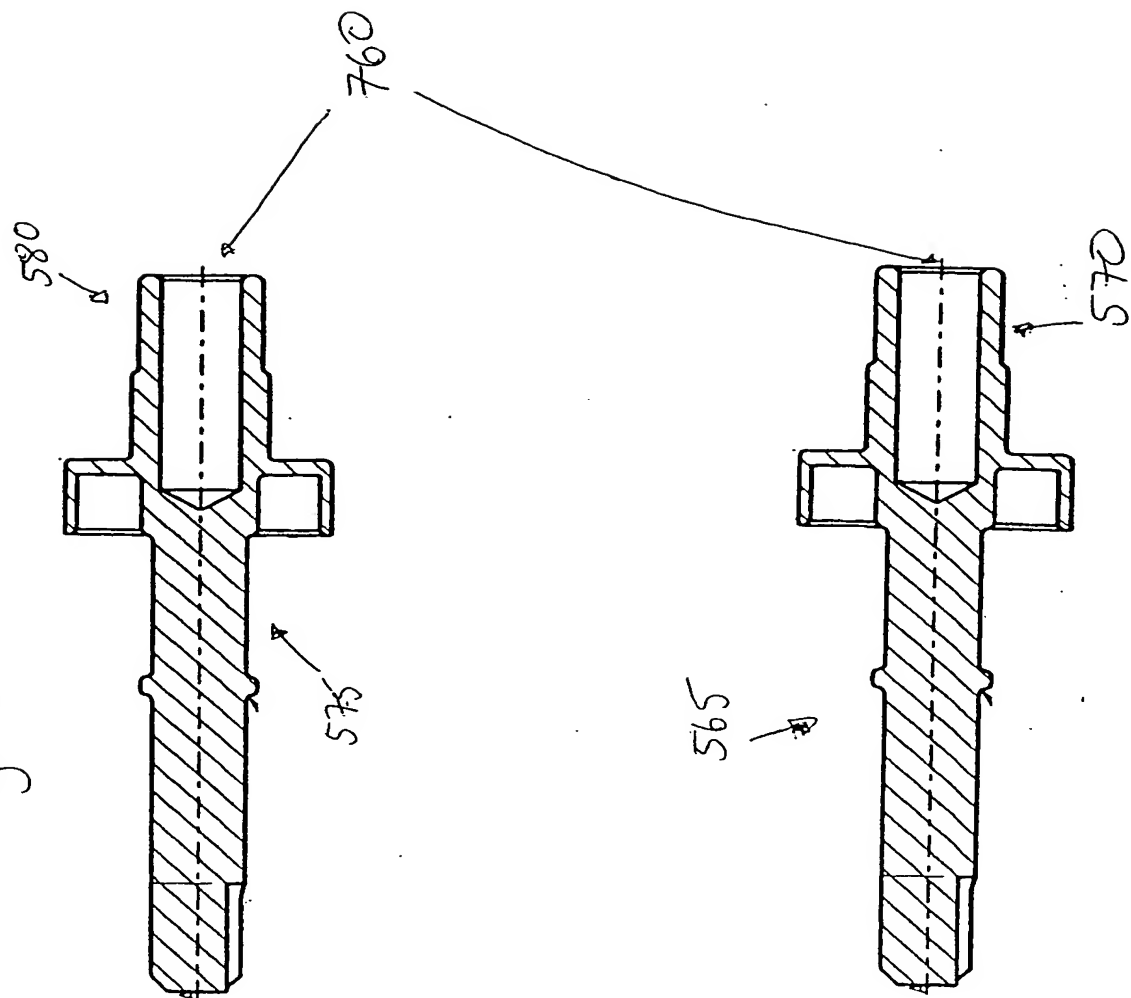


Figure 28a

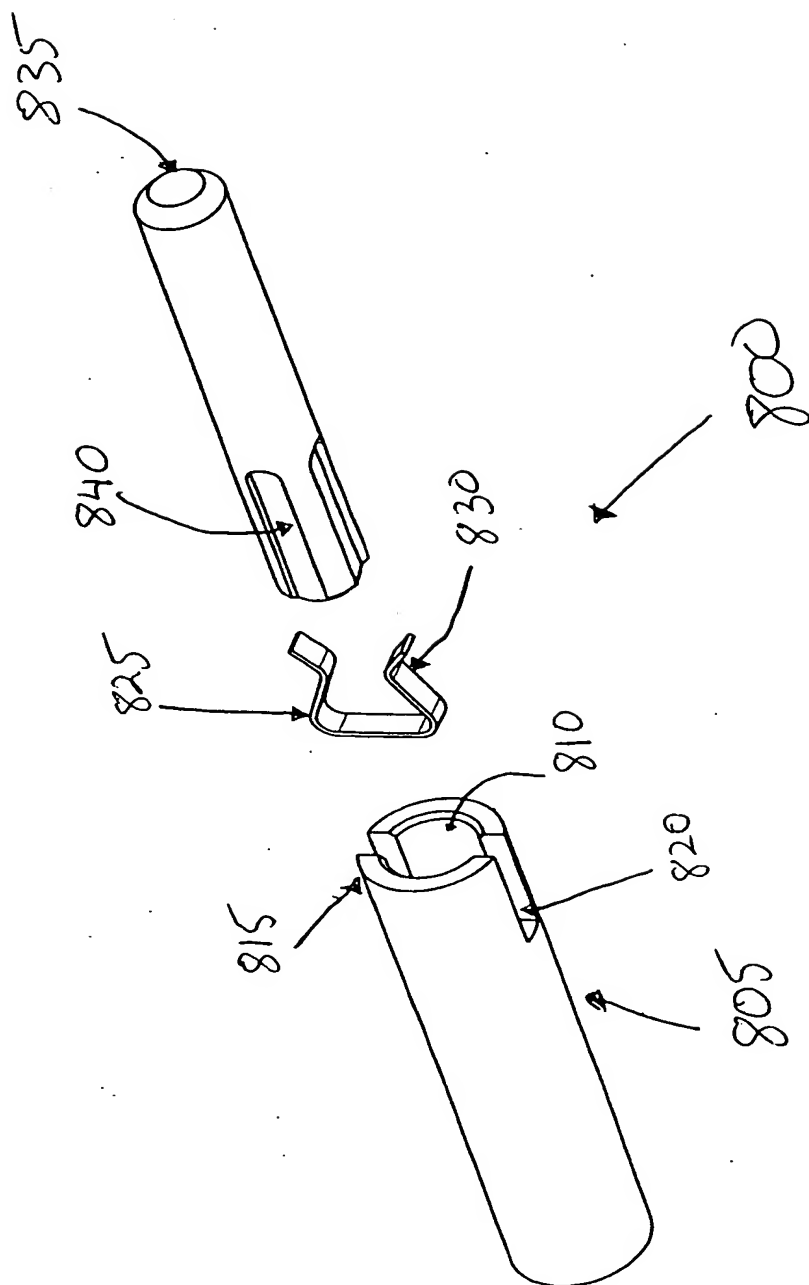


Figure 28b

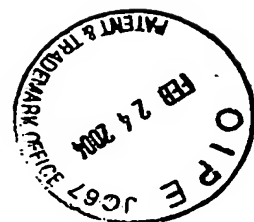
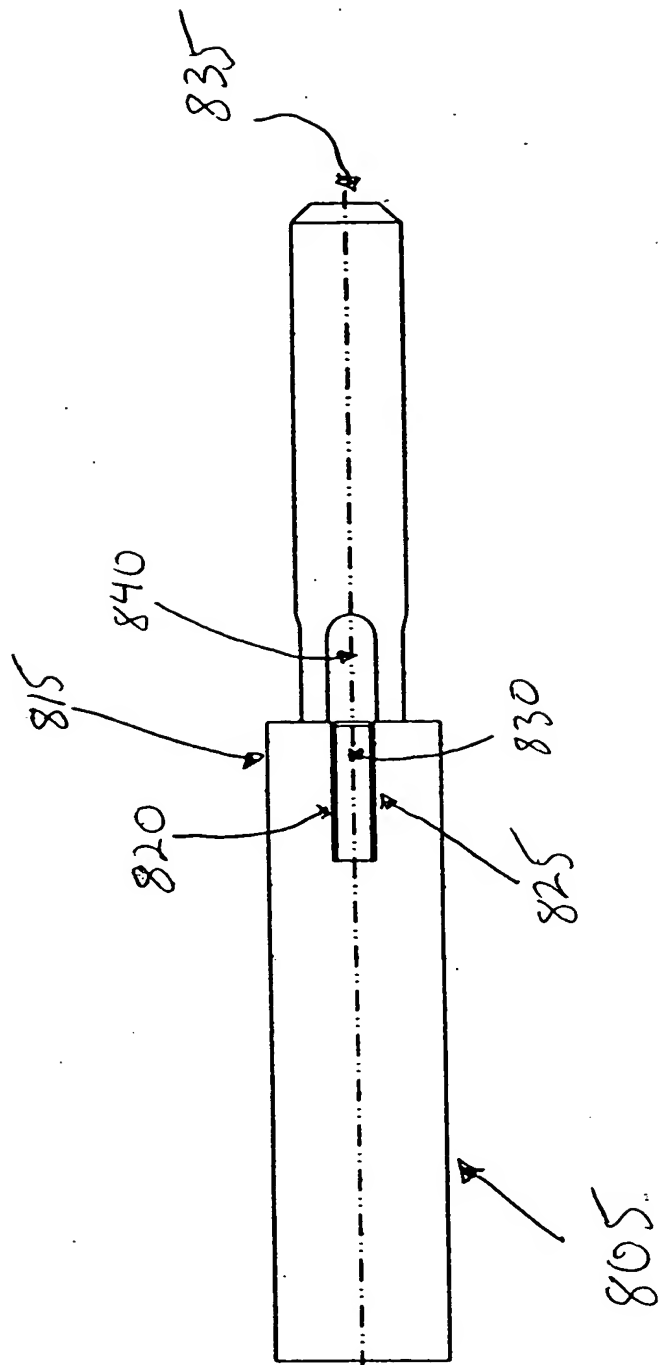


Figure 28C

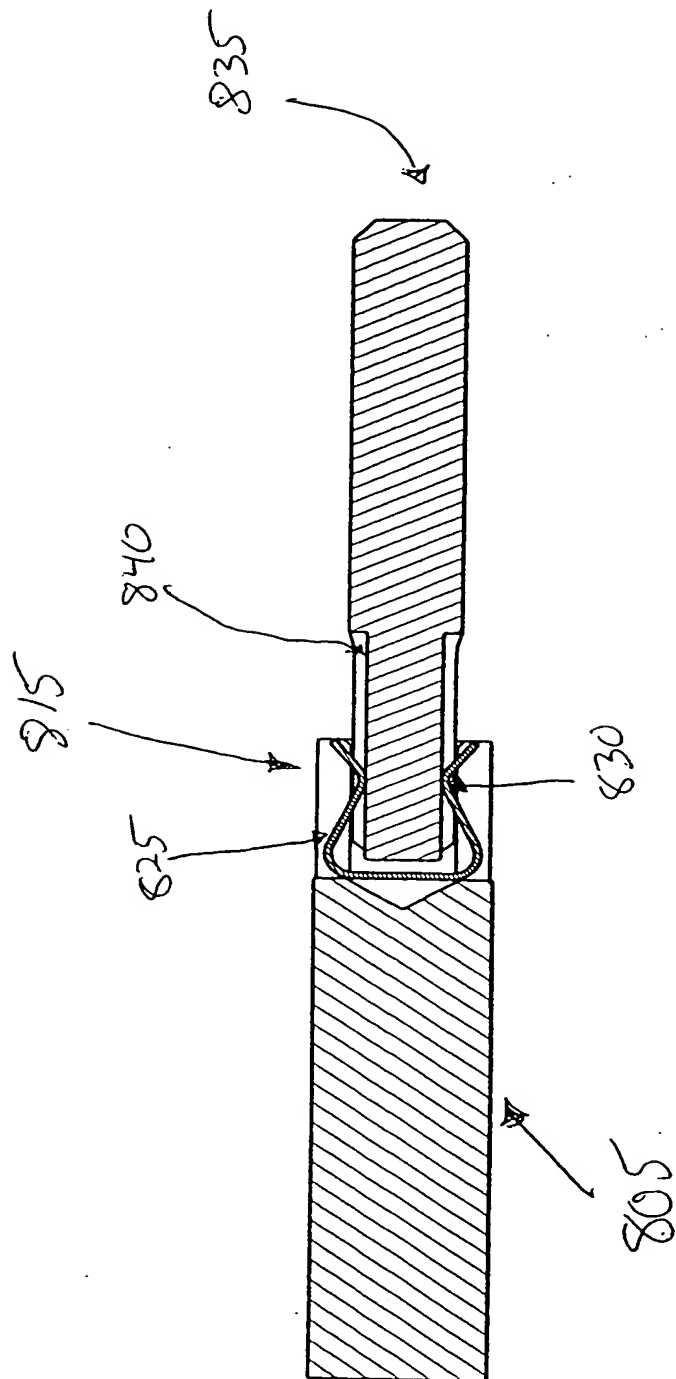


Figure 29

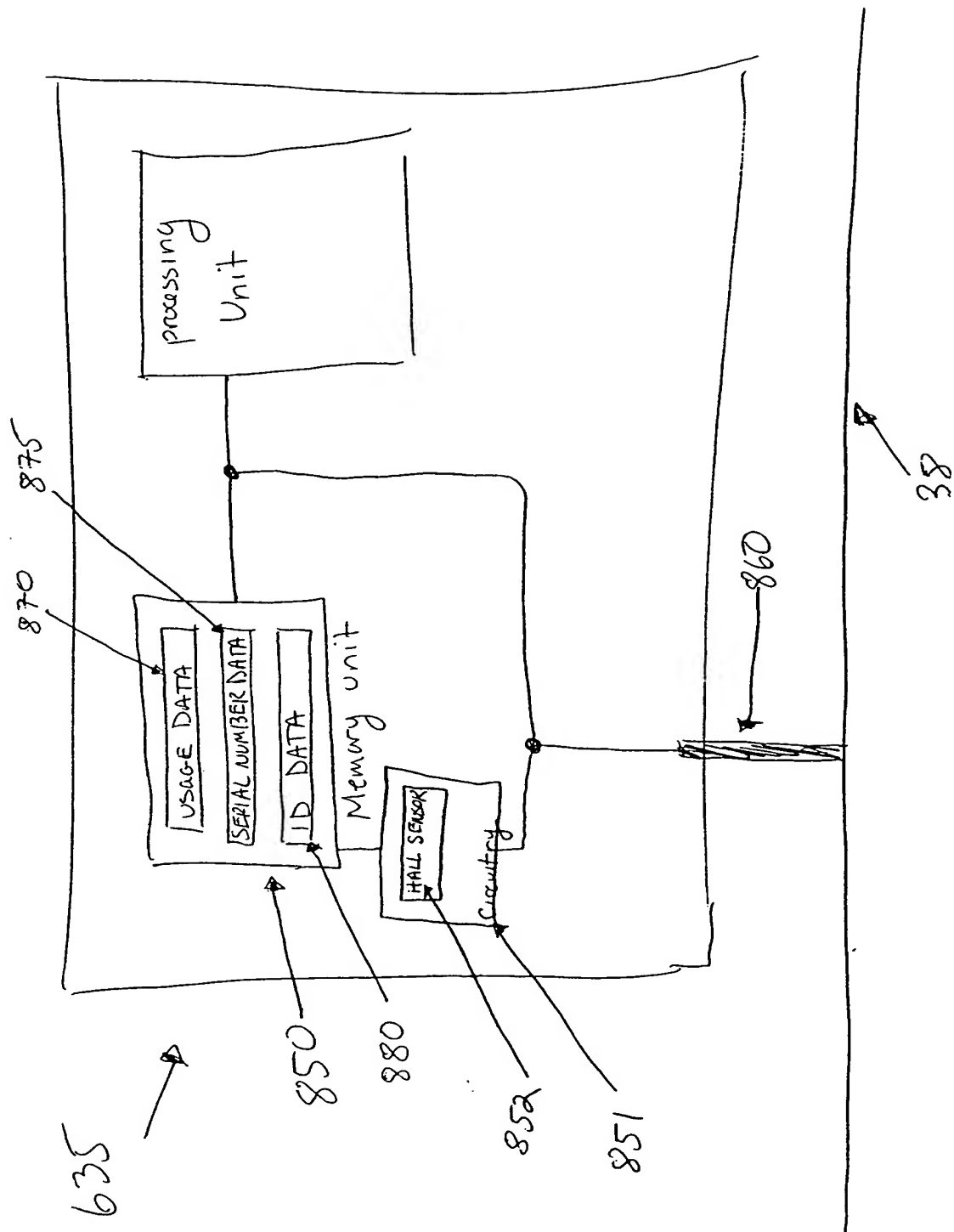


Figure 30a

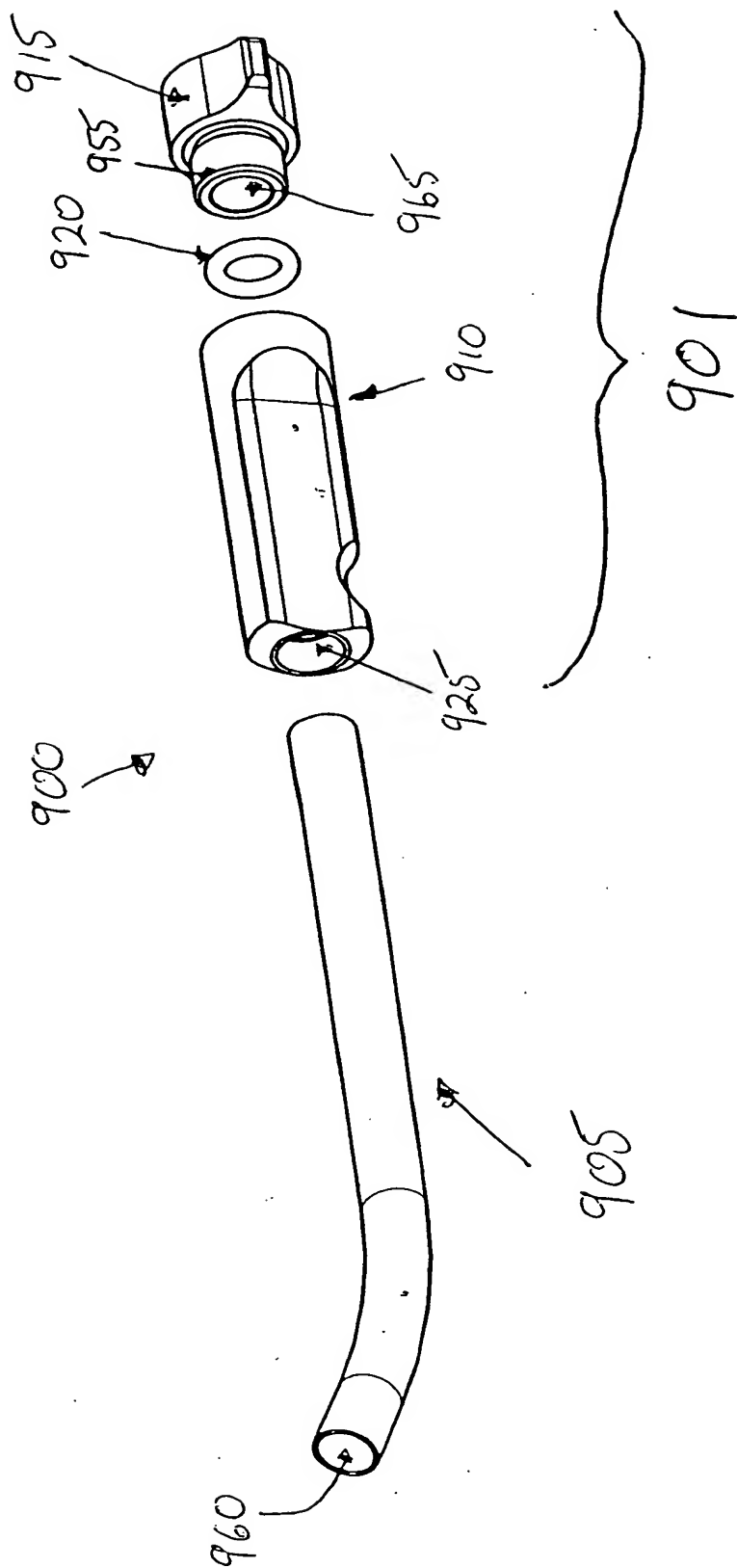


Figure 3Db

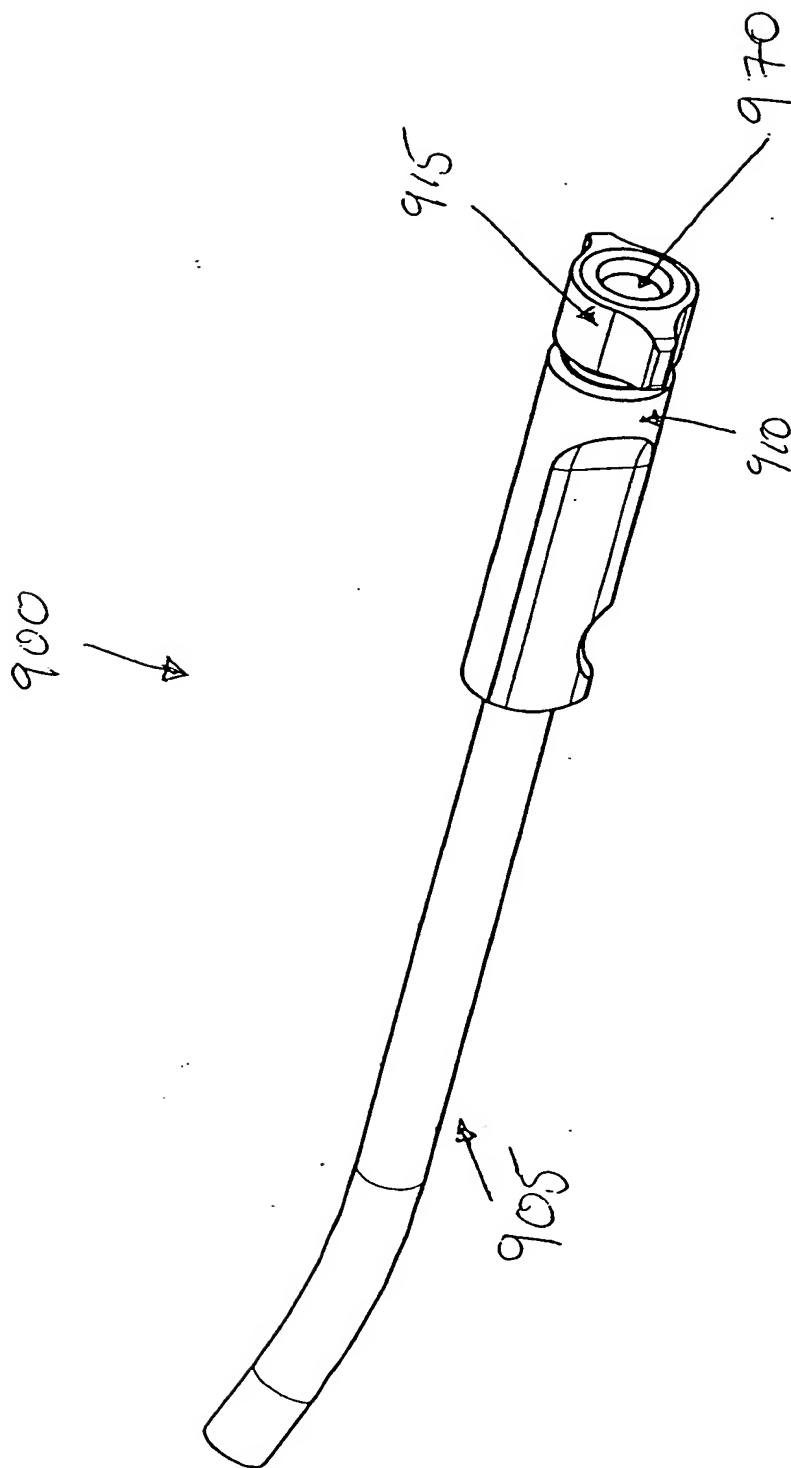
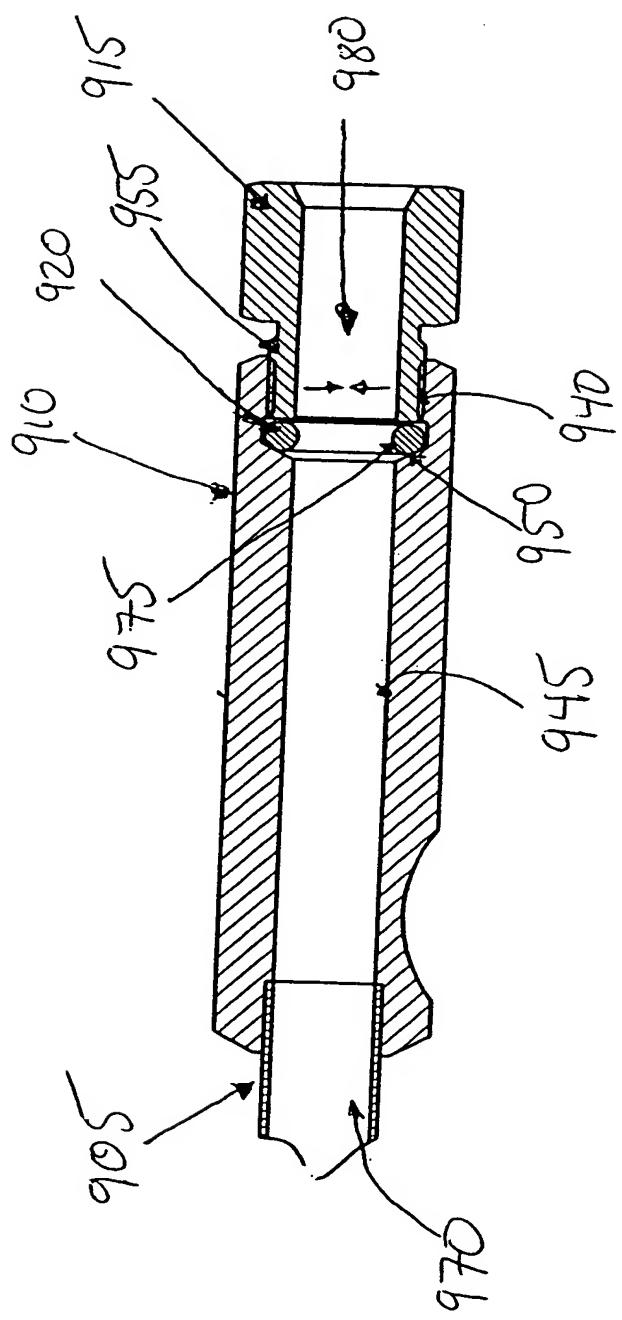


Figure 30C

900



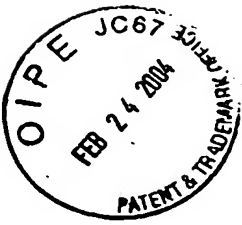


Figure 31

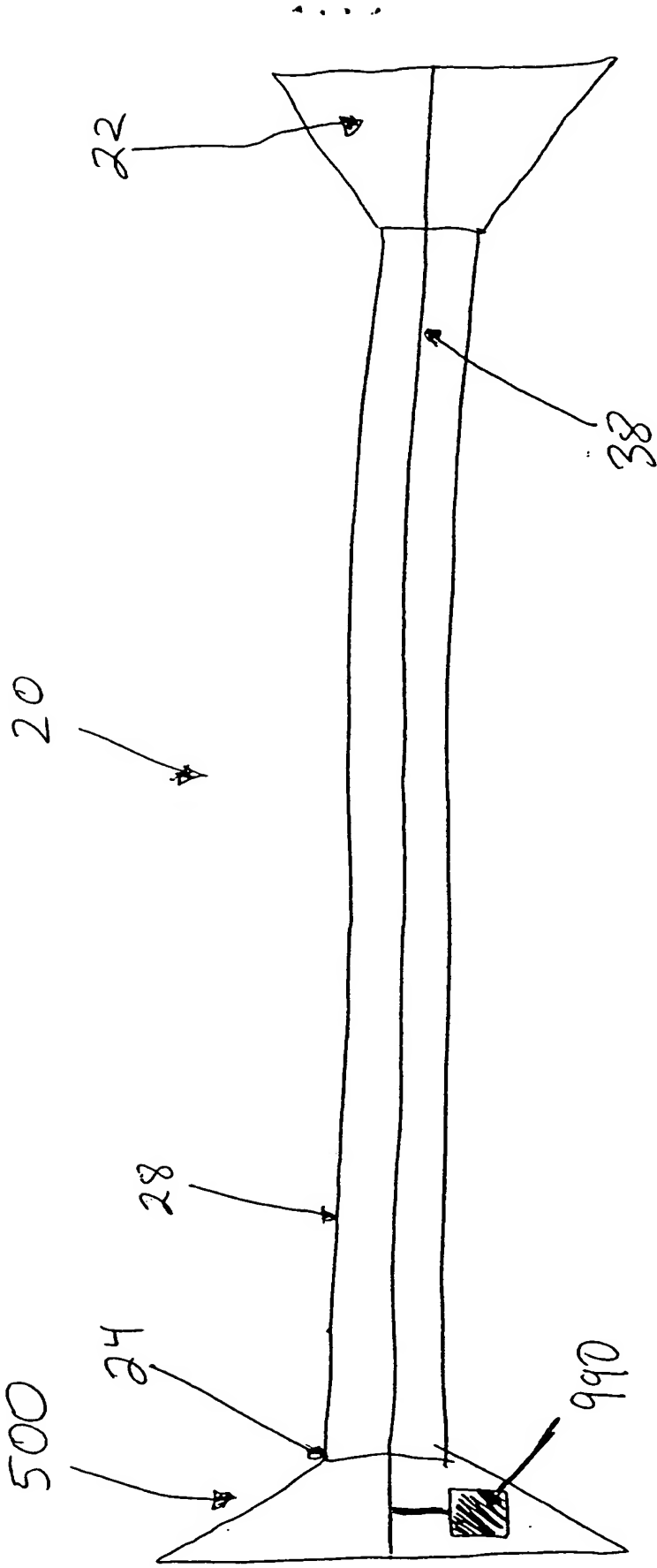
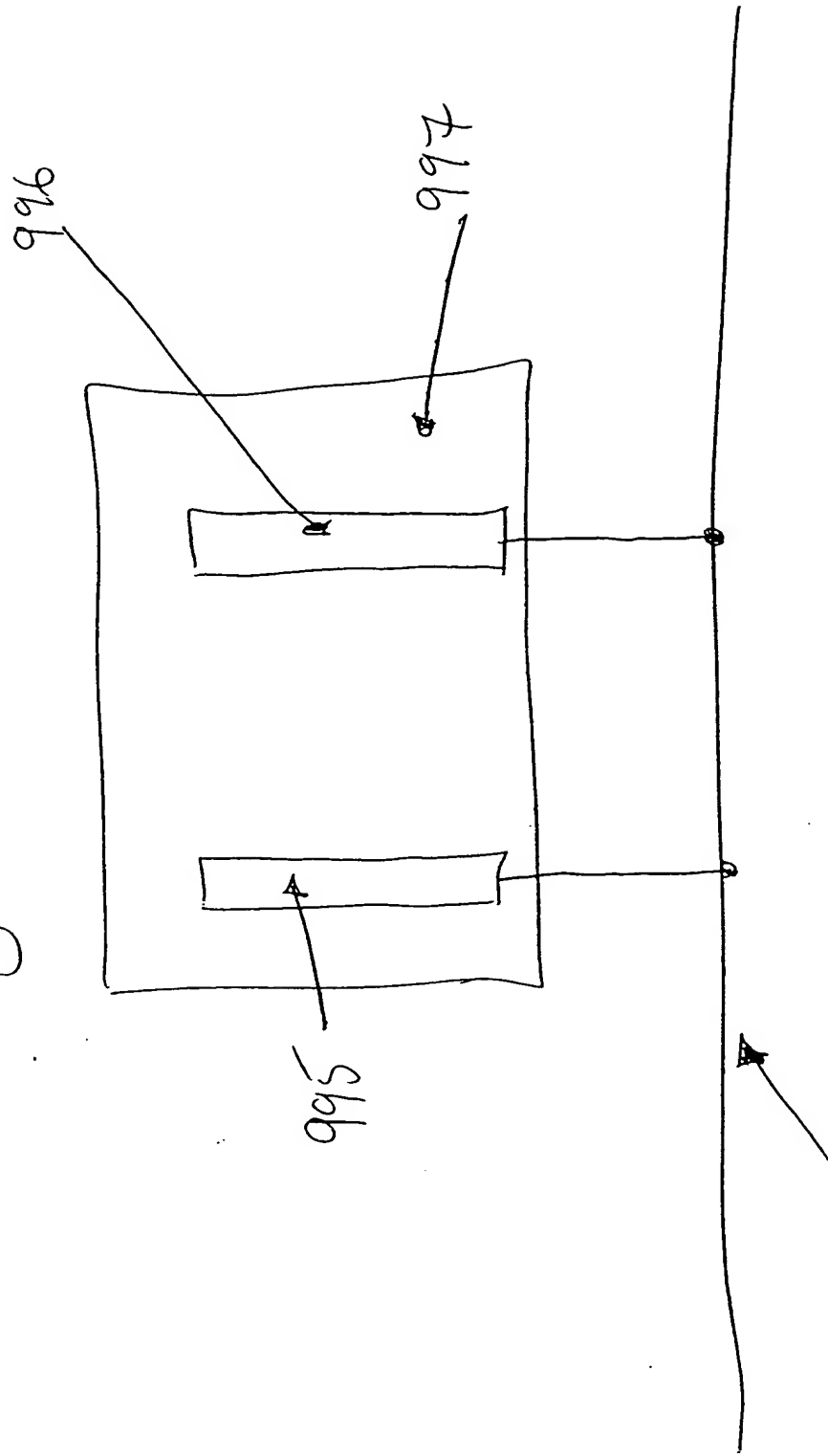


Figure 32



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